

INTRODUCTION

The DL9086 is a CMOS LSI which contains all the logic necessary to implement a five-function six-digit liquid crystal display watch. The circuit contains an oscillator amplifier with an internal feedback resistor for the use of 32,768Hz quartz crystals. The circuit operates from a single 1.5-volt battery and contains an internal voltage doubler. Only 2 switches are required to control all functions. These switch inputs have a pull down resistor and can be debounced by internal circuitry.

FUNCTIONS

- 5-functions: Month, Date, Hour, Minute and Second
- 12-hour format.
- Selectable display for hour, minute, second/month, date.
- One-touch correction of time error within ±30 seconds.
- 2-switch sequential operation.
- 4-year calendar.
- LCD test

FEATURES

- Simple Application circuit
- One-chip CMOS construction.
- Drives 5½ -digit triplexes LCD.
- Colon and PM display.
- Low power consumption.
- 32,768Hz ± 20ppm crystal oscillator.
- Single 1.5V battery operation.
- Built-in voltage doubler circuit.
- Built-in crystal oscillator input capacitor.
- Trimmer capacitor included
- Reset on power-on.

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

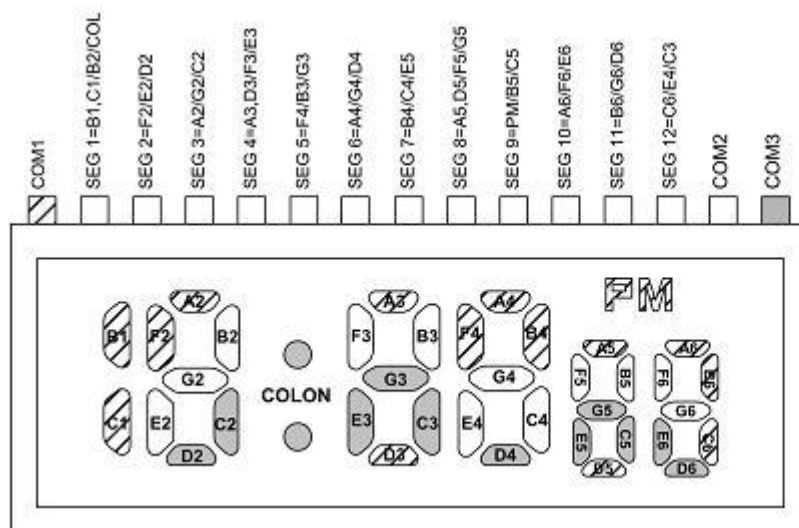
Parameter	Symbol	Value	Unit
Supply Voltage (V _{DD} - V _{SS})	V _{DS1}	- 0.3 ~ + 2.0	V
Operating Temperature	T _{OPN}	- 10 ~ + 70	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

*Voltage greater than above may damage the circuit.

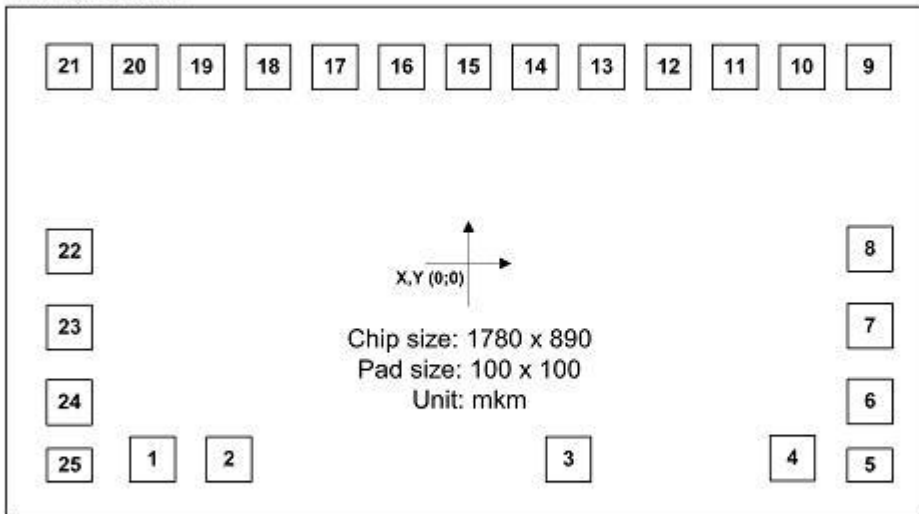
ELECTRICAL CHARACTERISTICS (Ta = 25°C, V_{DD} = 1.5V, V_{SS} = 0V; unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Operating Voltage	V _{DD}		1.2	1.5	1.8	V
Supply Current	I _{DD}	Without Load		1.5	2.0	μA
Input High Voltage	V _{IH}		V _{DD} - 0.3		V _{DD}	V
Input Low Voltage	V _{IL}		V _{SS}		V _{SS} + 0.3	V
Switch Activation Current	I _{SW}	V _{IN} = V _{DD}	0.1	0.5	5	μA
Oscillator Start Voltage	V _{OSC}	Within 5 sec			1.45	V
Oscillator Stop Voltage	V _{OSP}				1.15	V
Oscillator Frequency	F _{OSC}			32.768		Hz
LCD Frequency	F _d			43		Hz
Oscillator Capacitor	C _N			22		pF
	C _{OUT}			22		pF
Switch Debouncing Time	T _{DEB}				31.25	msec

LCD FORMAT



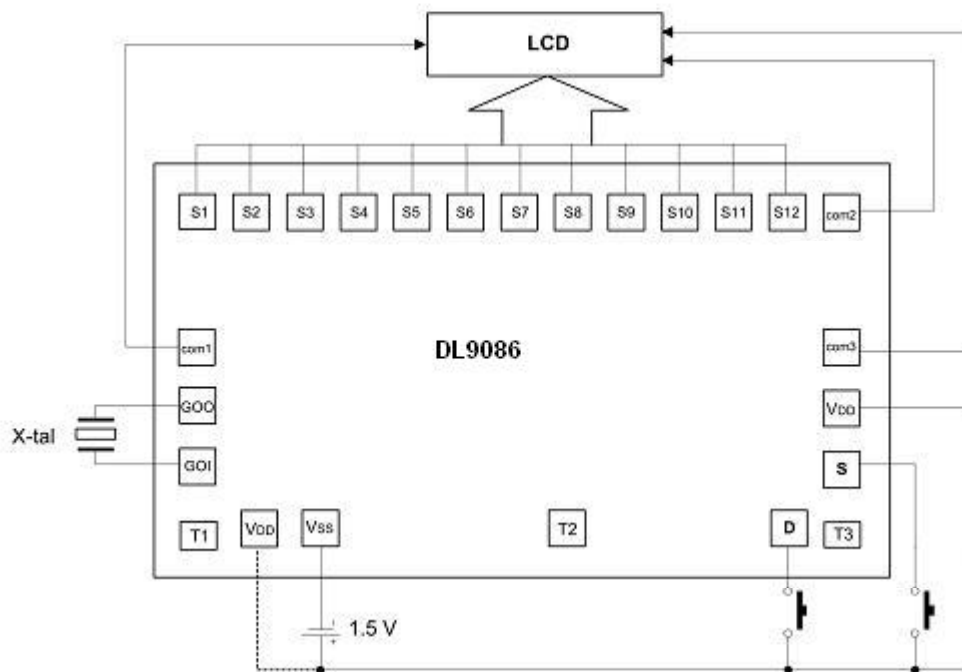
PAD DIAGRAM



PAD LOCATION

Pad No	Pad name	X(μm)	Y(μm)	Pad No	Pad name	X(μm)	Y(μm)
1	VDD	-610	-330	13	SEG9	258	330
2	VSS	-480	-330	14	SEG8	129	330
3	T2	171	-330	15	SEG7	0	330
4	D	616	-330	16	SEG6	-129	330
5	T3	769	-340	17	SEG5	-258	330
6	S	775	-226	18	SEG4	-387	330
7	VDD	775	-96	19	SEG3	-516	330
8	COM3	775	34	20	SEG2	-645	330
9	COM2	774	330	21	SEG1	-774	330
10	SEG12	645	330	22	COM1	-775	30
11	SEG11	516	330	23	GOO	-775	-100
12	SEG10	387	330	24	GOI	-775	-230
				25	T1	-757	-345

APPLICATION CIRCUIT



Substrate of chip is connected to VDD