



➤ **FUNCTIONS**

- \* 5 functions:month,date,hour,minute and second
- \* 12-hour format
- \* Selectable display for hour,minute,second/month,date
- \* One-touch correction of time error within±30 seconds
- \* 2-switch sequential operation
- \* 4-year calendar
- \* LCD test

功能

- 5 功能:月、日、时、分、秒
- 12 小时模式
- 可选择时,分,秒或月,日显示
- 按键一次可校正 30 秒时间误差
- 两键操作切换
- 四年日历
- LCD 测试
- 八段 LCD 动画显示

➤ **FEATURES**

- \* Single-chip CMOS constructions
- \* Drives 5.5-digit duplexed LCD
- \* Colon and PM display
- \* Low power dissipation
- \* 32768Hz crystal oscillator
- \* Single 1.5V battery operation
- \* Debounce circuitry on switch inputs
- \* Protection against static discharge
- \* Built-in crystal oscillator input capacitor
- \* Trimmer capacitor included

特点

- 单芯片, CMOS 制程
- 5.5 位 LCD 显示
- 冒号和下午标志显示
- 低功耗
- 标准 32768Hz 晶体
- 1.5V 电压工作
- 输入防反跳电路
- 输入静电保护
- 内置晶体振荡器输入电容
- 内含微调电容

➤ **ELECTRICAL CHARACTERISTICS** 电特性

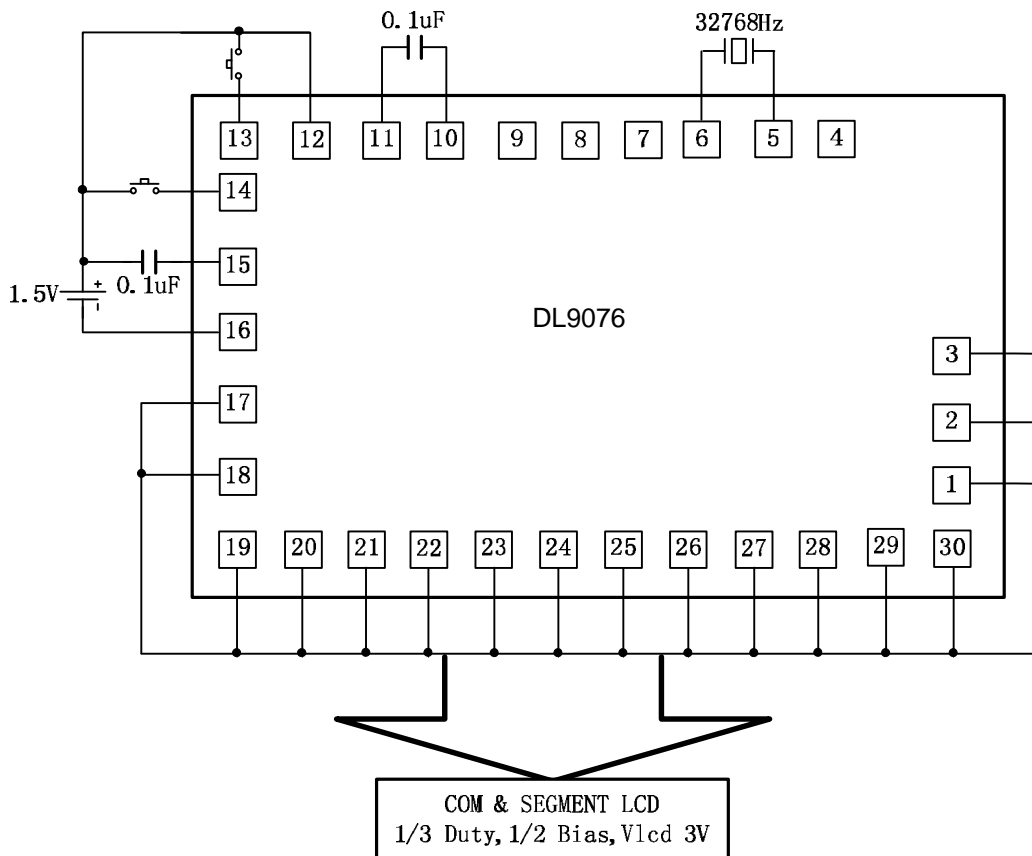
Characteristics	Sym	Min	Typ	Max	Unit	REMARKS
Operating Voltage 工作电压	Vdd	1.2	1.5	2	V	-
Operating Voltage 工作电压	Vss2	-1.2	-1.5	-2	V	-
Operating Current 工作电流	Iop	-	-	2.5	μA	Without Load
Input high voltage 输入高电压	Vih	Vdd-0.3	-	Vdd	V	-
Input low voltage 输出低电压	Vil	Vss	-	Vss+0.3	V	-
Oscillator Start Voltage 起振电压	Vosc			1.45	V	Within 5 Sec
Oscillator Stop Voltage 停振电压	Vosp			1.15	V	
Oscillator input Capacitor 振荡输入电容	CI		20	-	pF	
Oscillator Output Capacitor 振荡输出电容	CO		20		pF	
Oscillator input Frequency 振荡频率	Fosc	-	32768	-	Hz	
LCD frequency LCD 频率	FD	-	32	-	Hz	-



➤ APPLICATION INFORMATION 应用资料

Pad	Name	X,Y	Pad	Name	X,Y	Pad	Name	X,Y
1	SEG15	1585, 202	11	CAP1	355, 925	21	SEG10	345, 75
2	SEG14	1585, 334	12	VDD	215, 925	22	SEG9	477, 75
3	SEG13	1585, 466	13	D	75, 925	23	SEG8	610, 75
4	COM1	1336, 925	14	S	75, 785	24	SEG7	743, 75
5	OSCO	1195, 925	15	GND	75, 645	25	SEG6	875, 75
6	OSCI	1055, 925	16	VSS	75, 505	26	SEG5	1012, 75
7	T1	915, 927	17	COM3	75, 365	27	SEG4	1150, 75
8	T2	774, 927	18	COM2	75, 225	28	SEG3	1285, 75
9	T3	634, 927	19	SEG12	75, 75	29	SEG2	1423, 75
10	CAP2	495, 925	20	SEG11	213, 75	30	SEG1	1585, 75

➤ APPLICATION DIAGRAM 参考电路图

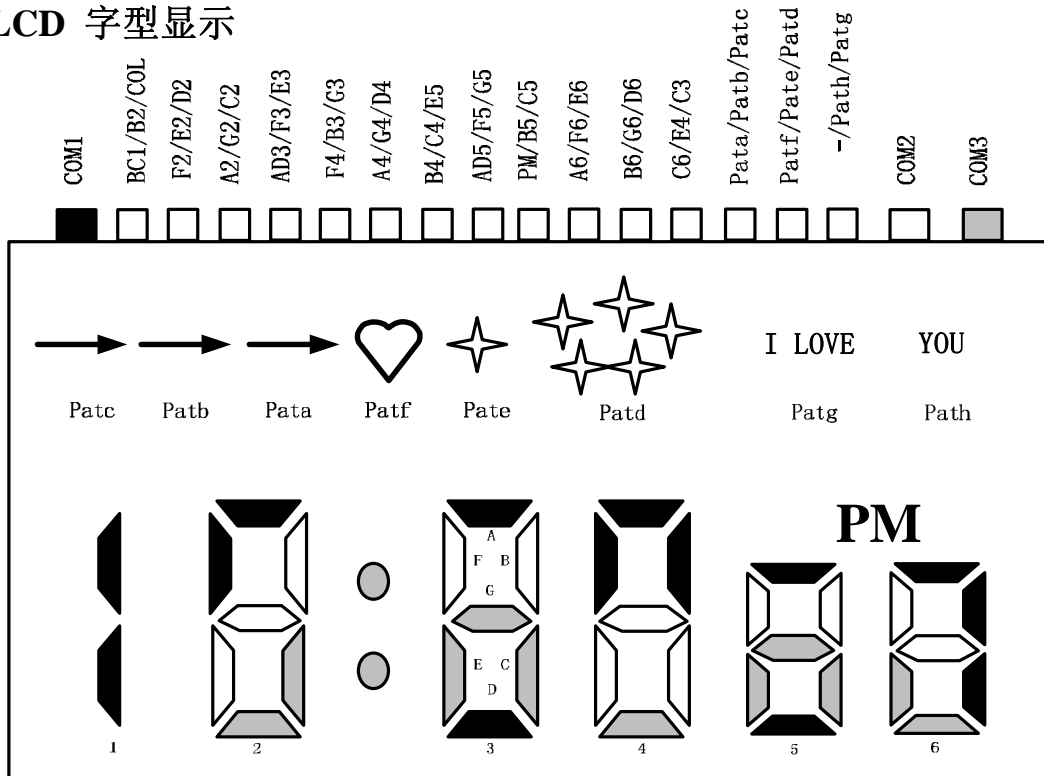


- 注:
1. 芯片衬底接 VDD;
  1. 片面积 1.66x 1.00mm<sup>2</sup>;
  2. PAD 坐标以芯片左下脚为原点。

➤ LCD 字段分配

	COM1	COM2	COM3
SEG1	BC1	B2	COL
SEG2	F2	E2	D2
SEG3	A2	G2	C2
SEG4	AD3	F3	E3
SEG5	F4	B3	G3
SEG6	A4	G4	D4
SEG7	B4	C4	E5
SEG8	AD5	F5	G5
SEG9	PM	B5	C5
SEG10	A6	F6	E6
SEG11	B6	G6	D6
SEG12	C6	E4	C3
SEG13	Pata	Patb	Patc
SEG14	Patf	Pate	Patd
SEG15	-	Path	Patg

➤ LCD 字型显示



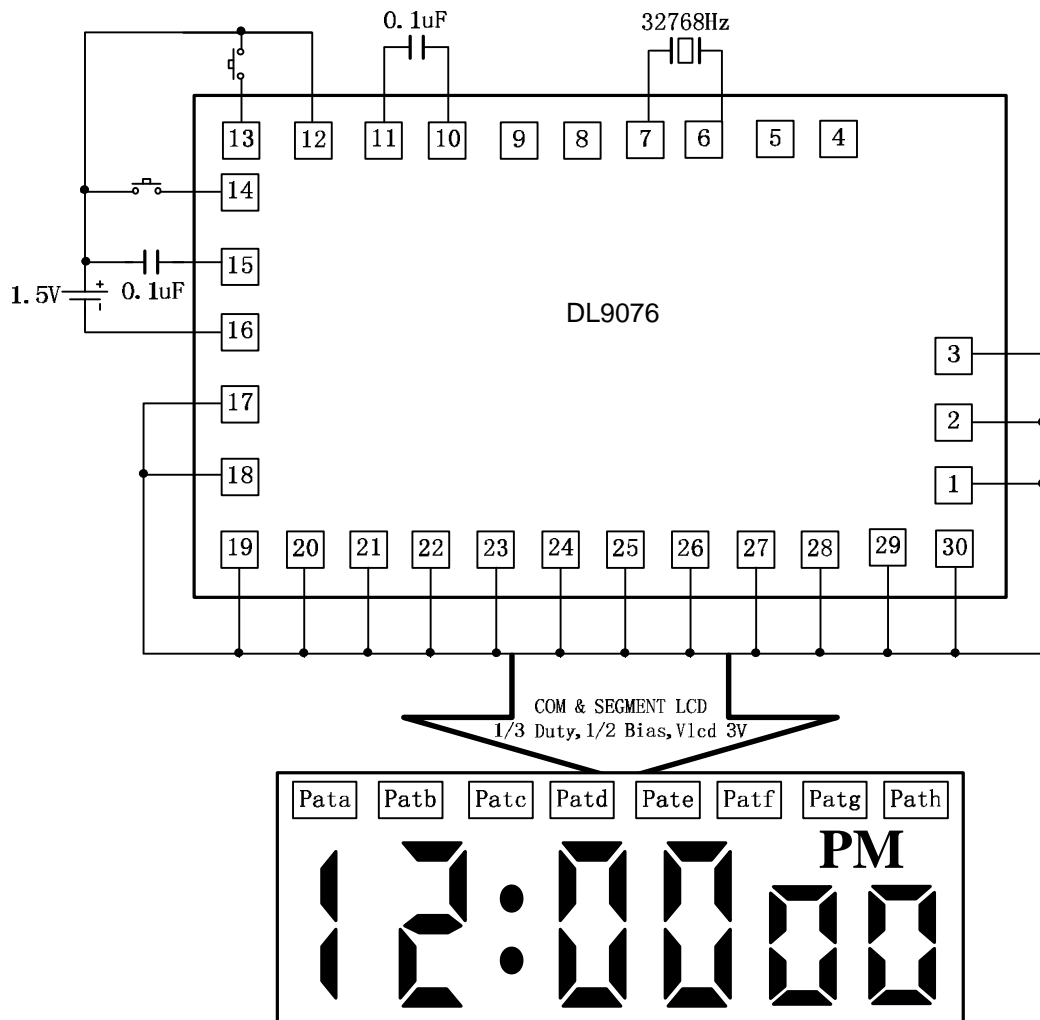
动画变化顺序: (8 段 LCD 每秒变化一步)

1. Patc
2. Patb
3. Pata
4. Patf
5. Patf, Pate
6. Patf, Pate, Patd
7. Patf, Pate, Patd, Patg
8. Patf, Pate, Patd, Patg, Path
9. 全熄;
10. 循环。

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➤ APPLICATION DIAGRAM 参考电路图



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