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## 1 KEY TOUCH PAD DETECTOR IC

### GENERAL DESCRIPTION

The DL7810 TonTouch™ is a touch pad detector IC which offers 1 touch key. The touching detection IC is designed for replacing traditional direct button key with diverse pad size. Low power consumption and wide operating voltage are the contact key features for DC or AC application.

### FEATURES

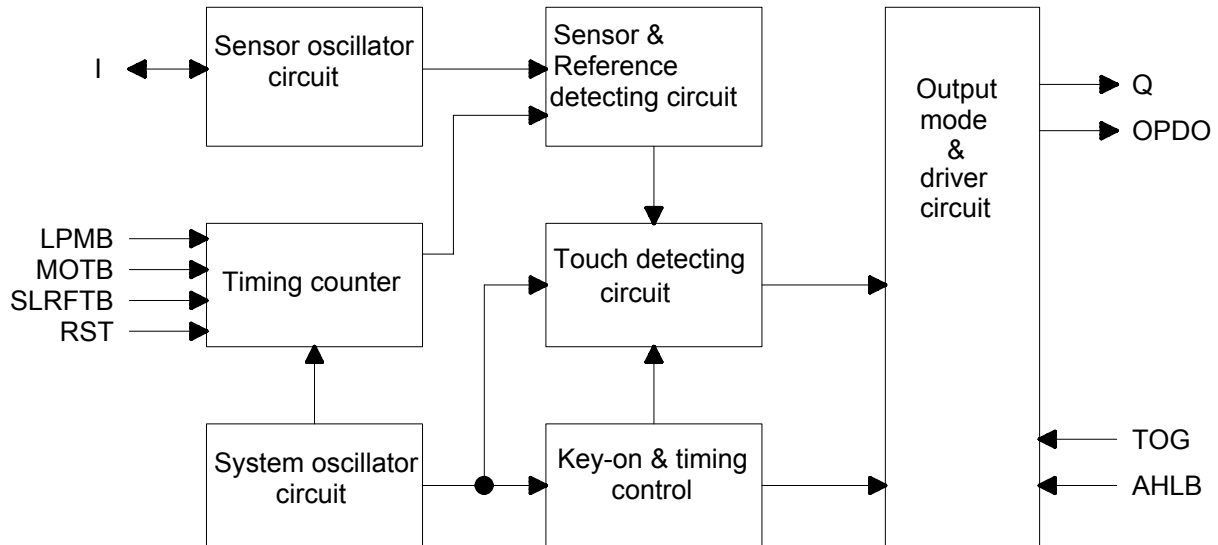
- Operating voltage 2.0V~5.5V
- Operating current @VDD=3V, no load, SLRFTB=1  
At low power mode typical 1.5uA, maximum 3.0uA  
At fast mode typical 3.5uA, maximum 7.0uA  
@VDD=3V, no load, SLRFTB=0  
At low power mode typical 2.0uA, maximum 4.0uA  
At fast mode typical 6.5uA, maximum 13.0uA
- The response time max about 60mS at fast mode, 220mS at low power mode @VDD=3V
- Sensitivity can adjust by the capacitance(0~50pF) outside
- Have two kinds of sampling length by pad option(SLRFTB pin)
- Stable touching detection of human body for replacing traditional direct switch key
- Provides Fast mode and Low Power mode selection by pad option(LPMB pin)
- Provides direct mode、toggle mode by pad option(TOG pin)  
Open drain mode by bonding option, OPDO pin is open drain output,  
Q pin is CMOS output
- All output modes can be selected active high or active low by pad option(AHLB pin)
- Have the maximum on time 100sec by pad option(MOTB pin)
- Have external power on reset pin(RST pin)
- After power-on have about 0.5sec stable-time, during the time do not touch the key pad,  
And the function is disabled
- Auto calibration for life  
And the re-calibration period is about 4.0sec, when key has not be touched at fast mode.  
At low power mode the re-calibration period is the same 4.0sec normally,  
When key detected touch and released touch, the auto re-calibration will be redoing after  
about 16sec from releasing key
- The sensitivity of DL7810N

### APPLICATION

- Wide consumer products
- Button key replacement



## BLOCK DIAGRAM



## PAD DESCRIPTION

Pad No.	Pad Name	I/O Type	Pad Description
1	Q	O	CMOS output pin
2	OPDO	OD	Open drain output pin
3	VSS	P	Negative power supply, ground
4	LPMB	I-PH	Low power mode selection, 1(Default)=>Fast mode; 0=>Low power mode
5	TOG	I-PL	Output type option pin, 1=>Toggle mode; 0(Default)=>Direct mode
6	VDD	P	Positive power supply
7	AHLB	I-PL	Output active high or low selection, 1=>Active low; 0(Default)=>Active high
8	RST	I-PL	External power on reset pin
9	SLRFTB	I-PH	Selecting the sampling length, 1(Default)=>about 1.6msec; 0=>about 3.2msec
10	MOTB	I-PH	Maximum on time 100sec selection, 1(Default)=>Disable, 0=>Enable
11	I	I/O	Input sensor port

### Pin Type

I	CMOS input only
O	CMOS push-pull output
I/O	CMOS I/O
OD	CMOS open drain output
I-PH	CMOS input and pull-high resistor
I-PL	CMOS input and pull-low resistor
P	Power / Ground



**ELECTRICAL CHARACTERISTICS**

• **Absolute Maximum Ratings**

Parameter	Symbol	Conditions	Value	Unit
Operating Temperature	T <sub>OP</sub>	—	-20 ~ +70	°C
Storage Temperature	T <sub>STG</sub>	—	-50 ~ +125	°C
Power Supply Voltage	VDD	Ta=25°C	VSS-0.3 ~ VSS+5.5	V
Input Voltage	V <sub>IN</sub>	Ta=25°C	VSS-0.3 ~ VDD+0.3	V
Human Body Mode	ESD	—	4	KV

Note : VSS symbolizes for system ground

• **DC/AC Characteristics** : (Test condition at room temperature=25°C)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit	
Operating Voltage	VDD		2.0	3	5.5	V	
System oscillator	F <sub>FAST</sub>	VDD=3V	-	512K	-	Hz	
	F <sub>LOW</sub>			16K			
Sensor oscillator	F <sub>SEN</sub>	VDD=3V no load	-	1M	-	Hz	
Operating Current	I <sub>OP</sub>	VDD=3V at low power mode and output no load	SLRFTB=1	-	1.5	3.0	uA
			SLRFTB=0	-	2.0	4.0	
		VDD=3V at fast mode and output no load	SLRFTB=1	-	3.5	7.0	
			SLRFTB=0		6.5	13.0	
Input Ports	V <sub>IL</sub>	Input Low Voltage	0	-	0.2	VDD	
Input Ports	V <sub>IH</sub>	Input High Voltage	0.8	-	1.0	VDD	
Output Port Sink Current	I <sub>OL</sub>	VDD=3V, V <sub>OL</sub> =0.6V	-	8	-	mA	
Output Port Source Current	I <sub>OH</sub>	VDD=3V, V <sub>OH</sub> =2.4V	-	-4	-	mA	
Output Response Time	T <sub>R</sub>	VDD=3V, At fast mode			60	mS	
		VDD=3V, At low power mode			220		
Input Pin Pull-high Resistor	R <sub>PH</sub>	VDD=3V, (LPMB, MOTB, SLRFTB)		35K		ohm	
Input Pin Pull-low Resistor	R <sub>PL</sub>	VDD=3V, (TOG, AHLB)		28K		ohm	
		VDD=3V, (RST)		200K			



## FUNCTION DESCRIPTION

### 1. Sensitivity adjustment

The total loading of electrode size and capacitance of connecting line on PCB can affect the sensitivity. So the sensitivity adjustment must according to the practical application on PCB. The DL7810/DL7810N offers some methods for adjusting the sensitivity outside.

1-1 by the electrode size

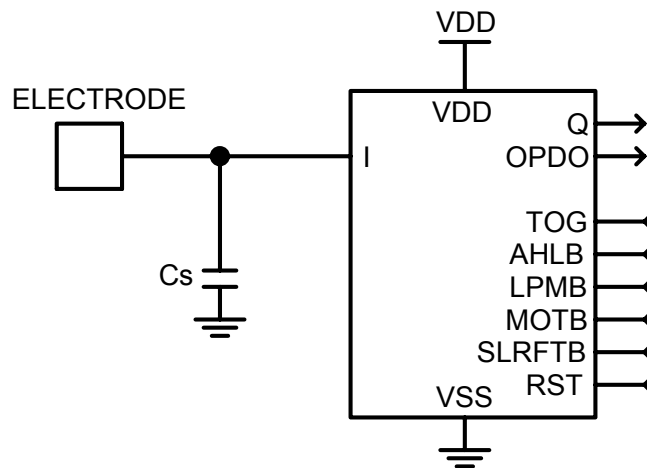
Under other conditions are fixed. Using a larger electrode size can increase sensitivity. Otherwise it can decrease sensitivity. But the electrode size must use in the effective scope.

1-2 by the panel thickness

Under other conditions are fixed. Using a thinner panel can increase sensitivity. Otherwise it can decrease sensitivity. But the panel thickness must be below the maximum value.

1-3 by the value of Cs (please see the down figure)

Under other conditions are fixed. When do not use the Cs to VSS, the sensitivity is most sensitive. When adding the values of Cs will reduce sensitivity in the useful range ( $0 \leq C_s \leq 50\text{pF}$ ).



### 2. Output mode

tive high or low by AHLB pad option. And has toggle mode by TOG pad option. Another has open drain mode by bonding option. Pad Q is digital output, pad OPDO is open drain output.

TOG	AHLB	Pad Q option features	Pad OPDO option features
0	0	Direct mode, CMOS active high output	Direct mode, Open drain active high output
0	1	Direct mode, CMOS active low output	Direct mode, Open drain active low output
1	0	Toggle mode, Power on state=0	Toggle mode, Power on state high-Z, Active high
1	1	Toggle mode, Power on state=1	Toggle mode, Power on state high-Z, Active low



### 3. Maximum key on duration time (By MOTB pad option)

If some objects cover in the sense pad, and causing the change quantity enough to be detected. To prevent this, the DL7810/DL7810N sets a timer to monitor the detection. The timer is the maximum on duration time. It is set about 100sec at 3V. When the detection is over the timer, the system will return to the power-on initial state, and the output becomes inactive until the next detection.

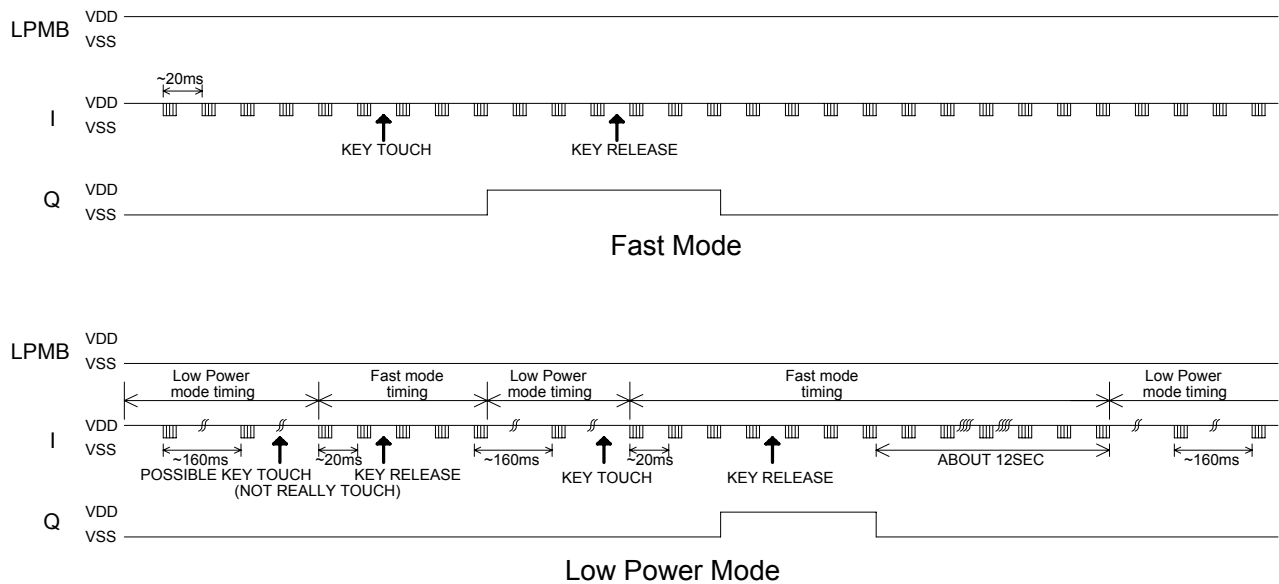
MOTB	Option features
1	Infinite(Disable maximum on time)
0	Maximum on time 100sec

### 4. Fast and Low power mode selection (By LPMB pad option)

The DL7810/DL7810N has Fast mode and Low Power mode to be selected. It depends on the state of LPMB pad. When the LPMB pin is opened or connected to VDD, the DL7810/DL7810N runs in Fast mode. When the LPMB pin is connected to VSS, the DL7810/DL7810N runs in Low Power mode.

In the Fast mode response time is faster, but the current consumption will be increased. In the Low Power mode it will be saving power, but will be slowing response time for first touch. When it awaked in fast mode, the response time is the same the fast mode. In this mode when detecting key touch, it will switch to Fast mode. Until the key touch is released and will keep a time about 12sec. Then it returns to Low Power mode.

The states and timing of two modes please see below figure.



LPMB	Option features
1	Fast mode
0	Low Power mode

### 5. Sampling length selection (By SLRFTB pad option)

The DL7810/DL7810N has two kinds of sampling length to be selected. It depends on the state of SLRFTB pad. When the SLRFTB pin is opened or connected to VDD, the sampling length is about 1.6msec. When the SLRFTB pin is connected to VSS, the sampling length is about 3.2msec.

When selecting 3.2msec sampling length the sensitivity will be better, but the current consumption will be increased. Recommend to use 1.6msec sampling length.

SLRFTB	Option features
1	Sampling length = 1.6msec
0	Sampling length = 3.2msec

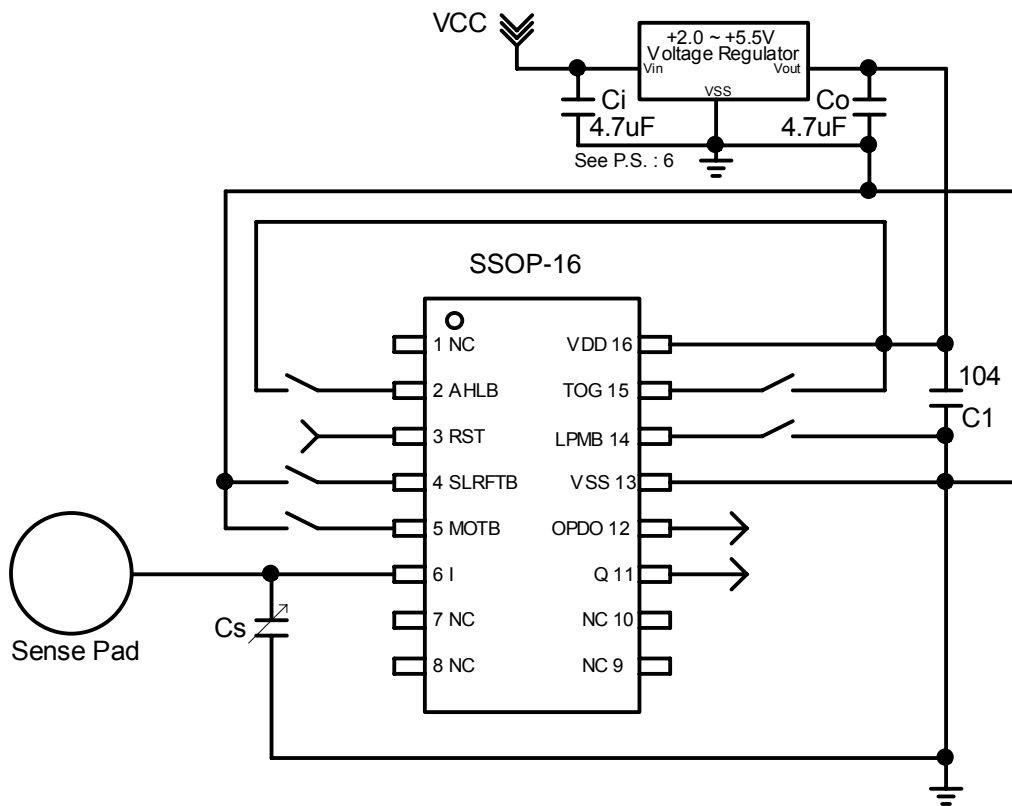


## 6. Option pin

For power saving concern and package bonding option consideration, all the feature option pins with latch type design and initial states are 0 or 1 as power on. If those pins are forced to VDD or VSS, the states will be changed to 1 or 0 without any current leakage to conflict the power saving issue.

Feature option pins	Initial state by Power on
AHLB	0
TOG	0
LPMB	1
MOTB	1
SLRFTB	1

## APPLICATION CIRCUIT



- PS : 1. On PCB, the length of lines from touch pad to IC pin shorter is better.  
And the lines do not parallel and cross with other lines.
2. The power supply must be stable. If the supply voltage drift or shift quickly, maybe causing sensitivity anomalies or false detections.
  3. The material of panel covering on the PCB can not include the metal or the electric element. The paints on the surfaces are the same.
  4. The capacitance Cs can be used to adjust the sensitivity. The value of Cs use smaller, then the sensitivity will be better. The sensitivity adjustment must according to the practical application on PCB. The range of Cs value is 0~50pF.
  5. The C1 capacitor must be used between VDD and VSS; and should be routed with very short tracks to the device's VDD and VSS pins (DL7810/DL7810N).
  6. The value of capacitors can be used by the real application for Ci and Co capacitors.
  7. The sensitivity adjustment capacitors (Cs) must use smaller temperature coefficient and more stable capacitors. Such are X7R, NPO for example. So for touch application, recommend to use NPO capacitor, for reducing that the temperature varies to affect sensitivity.

## PACKAGE LIST

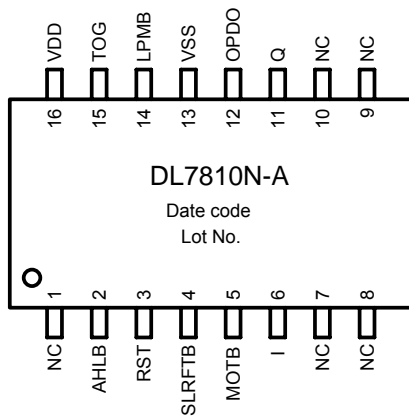
### 1. DL7810-ASB and DL7810N-ASB

Package Item	Package Type	Option Features							
		AHLB	TOG	LPMB	MOTB	SLRFTB	RST	Q	OPDO
DL7810N-ASB	SSOP-16	V	V	V	V	V	V	V	V
DL7810N-ASB	SSOP-16	V	V	V	V	V	V	V	V

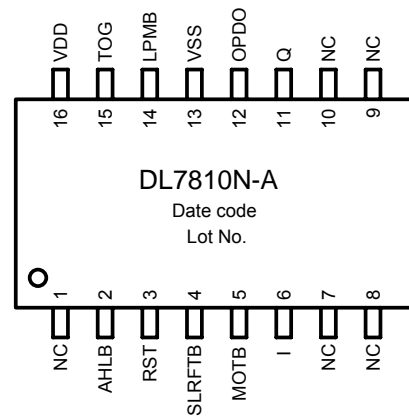
P.S.: The mark of "V" express that package item has the option feature pin.

## PACKAGE CONFIGURATION

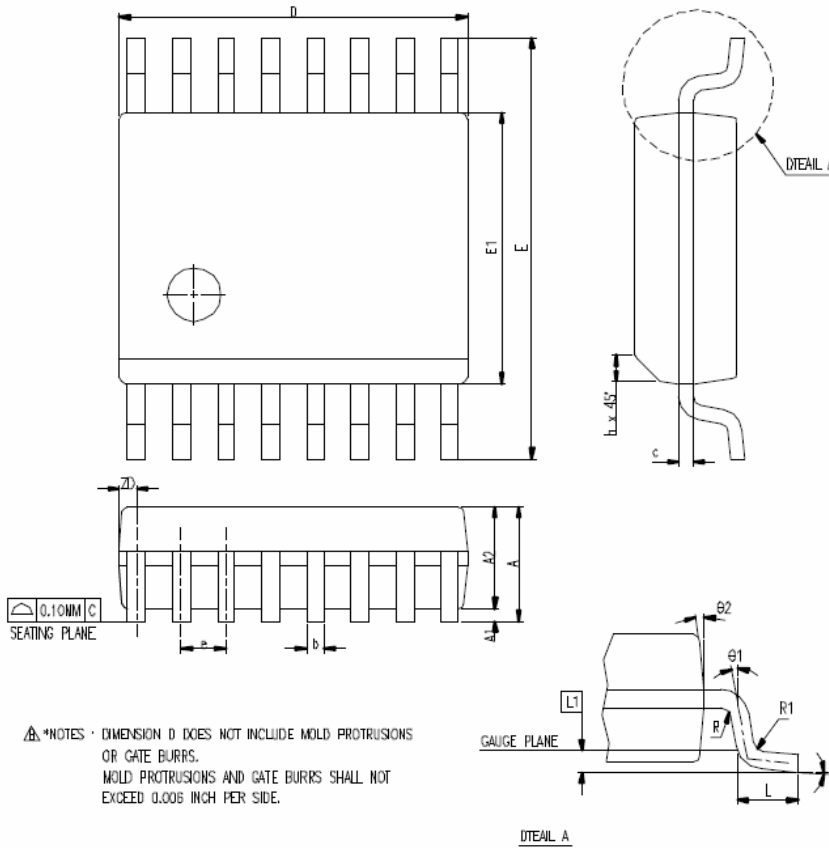
### DL7810N-ASB



### DL7810N-ASB



## Package Outline Dimension



SYMBOL	DIMENSION IN MM			DIMENSION IN INCH		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	1.35	1.63	1.75	0.053	0.064	0.069
A1	0.10	0.15	0.25	0.004	0.006	0.010
A2			1.50			0.059
b	0.20		0.30	0.008		0.012
c	0.18		0.25	0.007		0.010
e	0.635 BASIC			0.025 BASIC		
D	4.80	4.90	5.00	0.189	0.193	0.197
E	5.79	5.99	6.20	0.228	0.236	0.244
E1	3.81	3.91	3.99	0.150	0.154	0.157
L	0.41	0.635	1.27	0.016	0.025	0.050
h	0.25		0.50	0.010		0.020
L1	0.254 BASIC			0.010 BASIC		
ZD	0.229 REF			0.009 REF		
R1	0.20		0.33	0.008		0.013
R	0.20			0.008		
theta	0°		8°	0°		8°
theta1	0°			0°		
theta2	5°	10°	15°	5°	10°	15°
JEDEC	MO-137 (AB)					

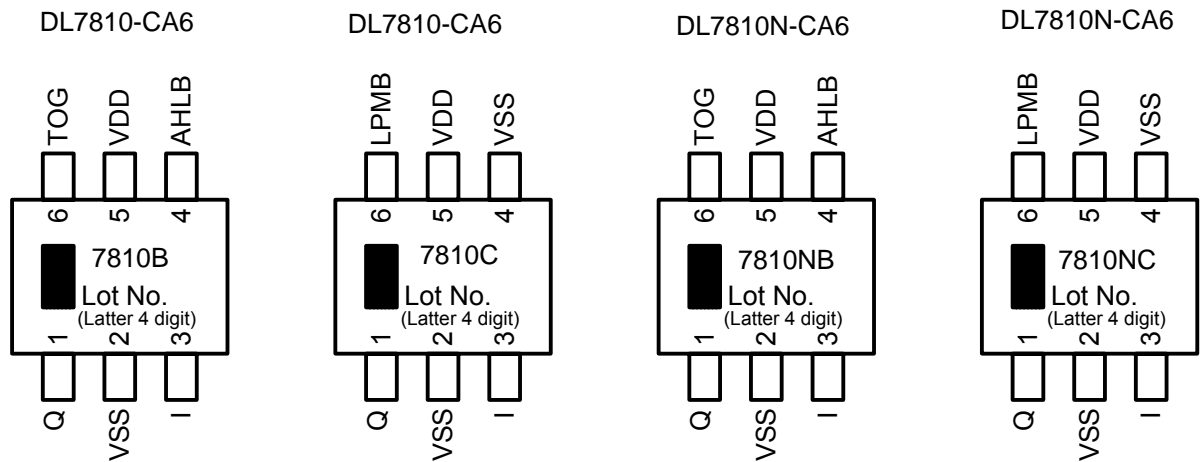


## 2. DL7810-BA6 & DL7810-CA6 and DL7810N-BA6 & TTP223N-CA6

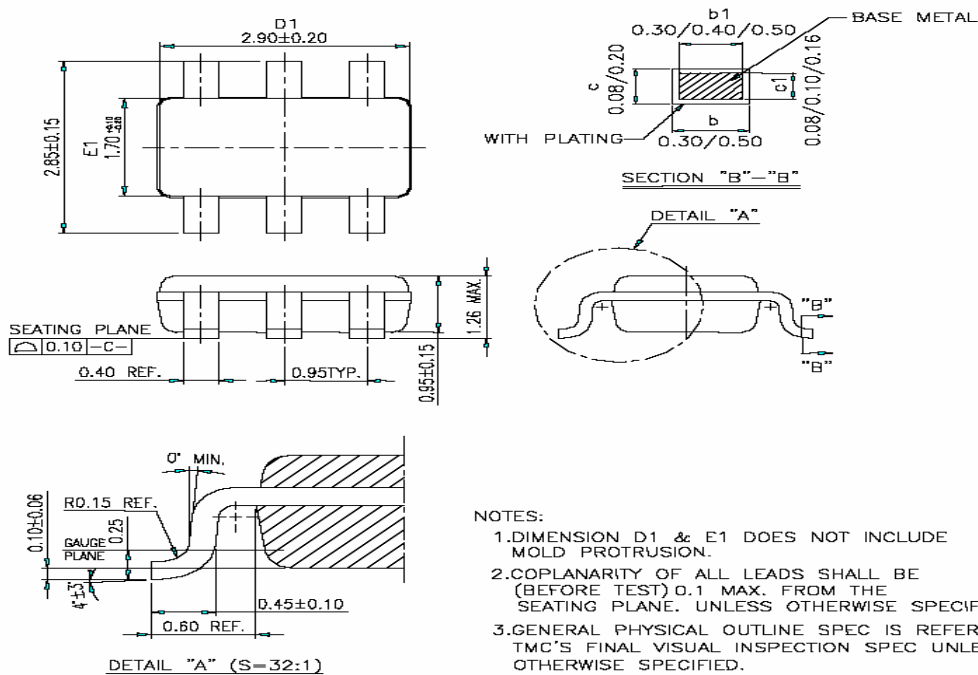
Package Item	Package Type	Option Features							
		AHLB	TOG	LPMB	MOTB	SLRFTB	RST	Q	OPDO
DL7810-BA6	SOT-23-6L	V	V	0	1	1	X	V	X
DL7810N-BA6	SOT-23-6L	V	V	0	1	0	X	V	X
DL7810-CA6	SOT-23-6L	0	0	V	1	1	X	V	X
DL7810N-CA6	SOT-23-6L	0	0	V	1	0	X	V	X

P.S.: The mark of "V" express that package item has the option feature pin.  
The mark of "X" express that package item has no the option feature pin.  
The mark of "0" express that the state of option pin is fixed to VSS in the package item.  
The mark of "1" express that the state of option pin is fixed to VDD in the package item.

## DL7810-BA6 & DL7810-CA6 and DL7810N-BA6 & DL7810N-CA6 PACKAGE CONFIGURATION



## Package Outline Dimension



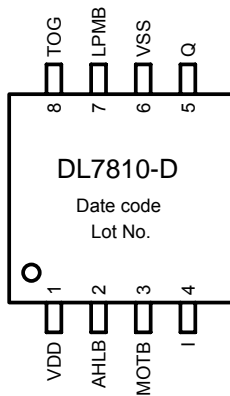
### 3. DL7810-DO8 and DL7810N-DO8

Package Item	Package Type	Option Features							
		AHLB	TOG	LPMB	MOTB	SLRFTB	RST	Q	OPDO
DL7810-DO8	SOP-8	V	V	V	V	1	X	V	X
DL7810N-DO8	SOP-8	V	V	V	V	0	X	V	X

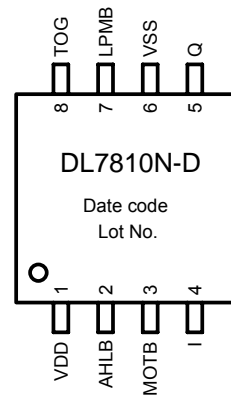
P.S.: The mark of "V" express that package item has the option feature pin.  
The mark of "X" express that package item has no the option feature pin.  
The mark of "0" express that the state of option pin is fixed to VSS in the package item.  
The mark of "1" express that the state of option pin is fixed to VDD in the package item.

### DL7810-DO8 and DL7810N-DO8 PACKAGE CONFIGURATION

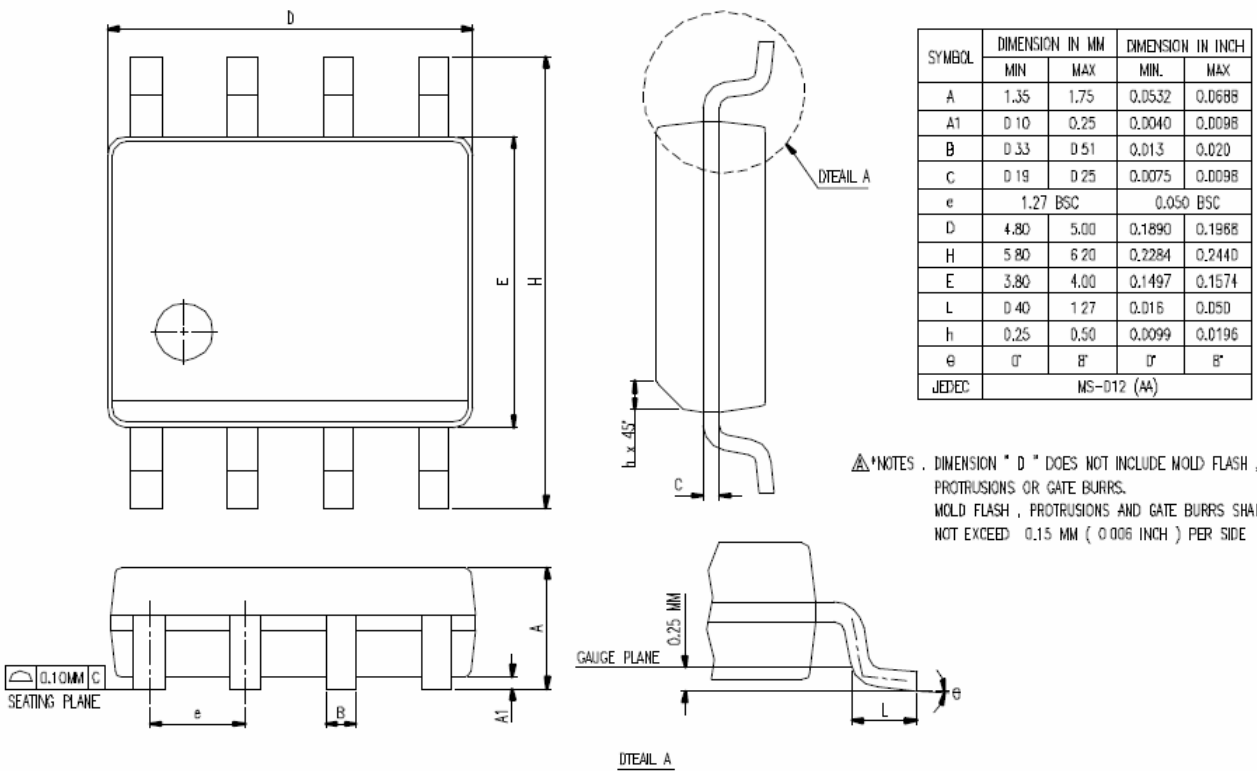
#### DL7810-DO8



#### DL7810N-DO8



### Package Outline Dimension



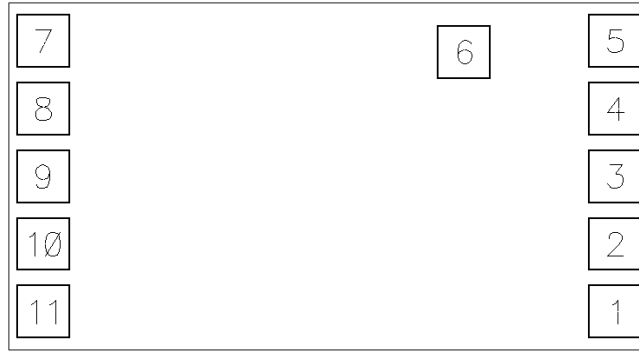


## ORDER INFORMATION

1. For DL7810
  - a. Package form: DL7810-XXX
  - b. Chip form: DL7810
  - c. Wafer base: DL7810
  
2. For DL7810N
  - a. Package form: DL7810N-XXX
  - b. Chip form: DL7810N
  - c. Wafer base: DL7810-02

## REVISE HISTORY

1. 2008/04/07
  - Original version : V\_1.0
2. 2008/04/21 => V\_1.1
  - A. On the page-2 pad description pad-4, 5, 7, 9, 10 add the word 'Default' description.
  - B. On the page-3 electrical characteristics add the item-Human Body Mode  $\geq 4KV$  for ESD.
3. 2008/06/10 => V\_1.2
  - A. On the page-1 FEATURES => Auto calibration for life .....  
APPLICATION => .....
  - B. On the page-2 PAD DESCRIPTION => Pin type .....
  - C. On the page-3 ELECTRICAL CHARACTERISTICS => Human Body Mode .....
  - D. On the page-7 APPLICATION CIRCUIT => Figure and PS 5, 6 .....
4. 2008/11/11 => V\_2.0
5. 2009/08/31 => V\_2.1
  - Add the PS 7. The sensitivity adjustment capacitors (Cs) ..... on the page-7.
  - Add the trademark " TonTouch™ " to every page.



OUTLINE SIZE: 1180 \* 680 um<sup>2</sup>

SUBSTRATE floating (recommend) or VSS

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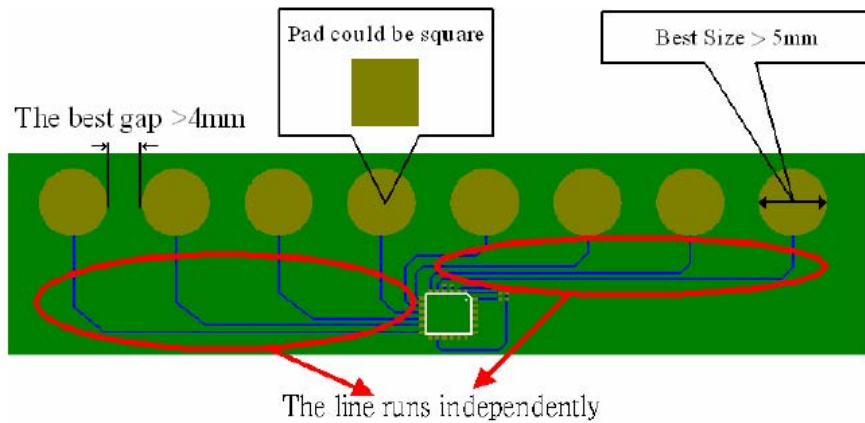
NO	NAME	X=	Y=
1	Q	490.00	-234.00
2	OPDO	490.00	-117.00
3	VSS	490.00	0.00
4	LPMB	490.00	117.00
5	TOG	490.00	234.00
6	VDD	233.60	215.00
7	AHLB	-490.00	234.00
8	RST	-490.00	117.00
9	SLRFTB	-490.00	0.00
10	MOTB	-490.00	-117.00
11	I	-490.00	-234.00

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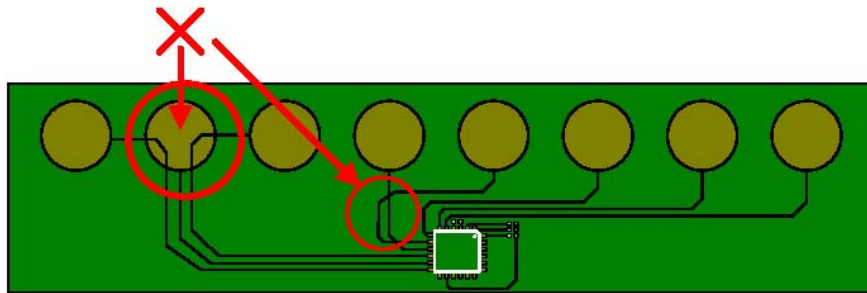


## Simple PCB Layout Guide

- PAD跑線要獨立
- PAD與跑線建議在不同層做佈局(若PAD在Top層，跑線建議在Bottom層)



- PAD跑線彼此間不能橫跨(會造成相互干擾)。



\* Touch PAD 不要使用大面积 GND 包围,建议最少预留 0.4mm 以上的安全距离.