

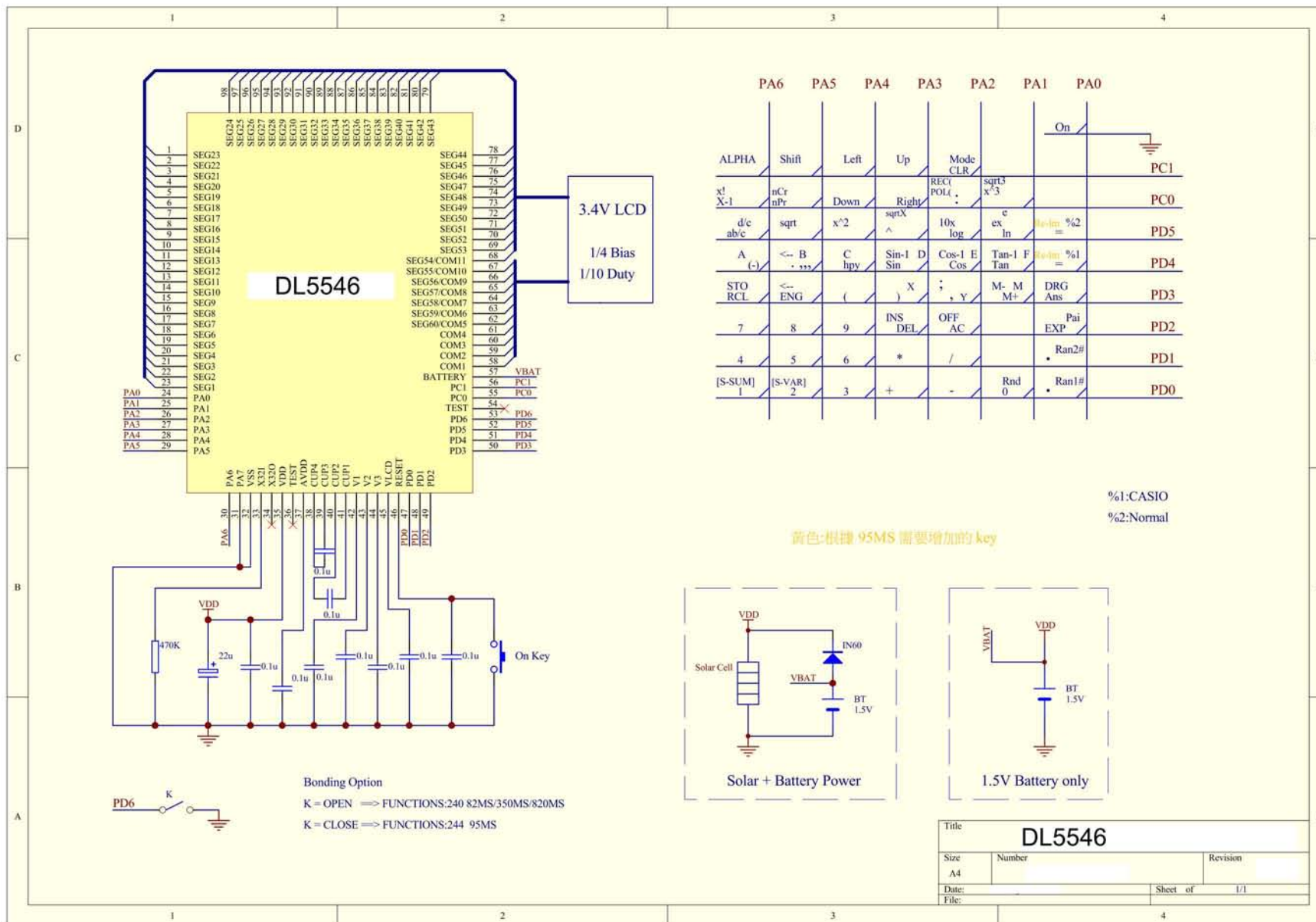


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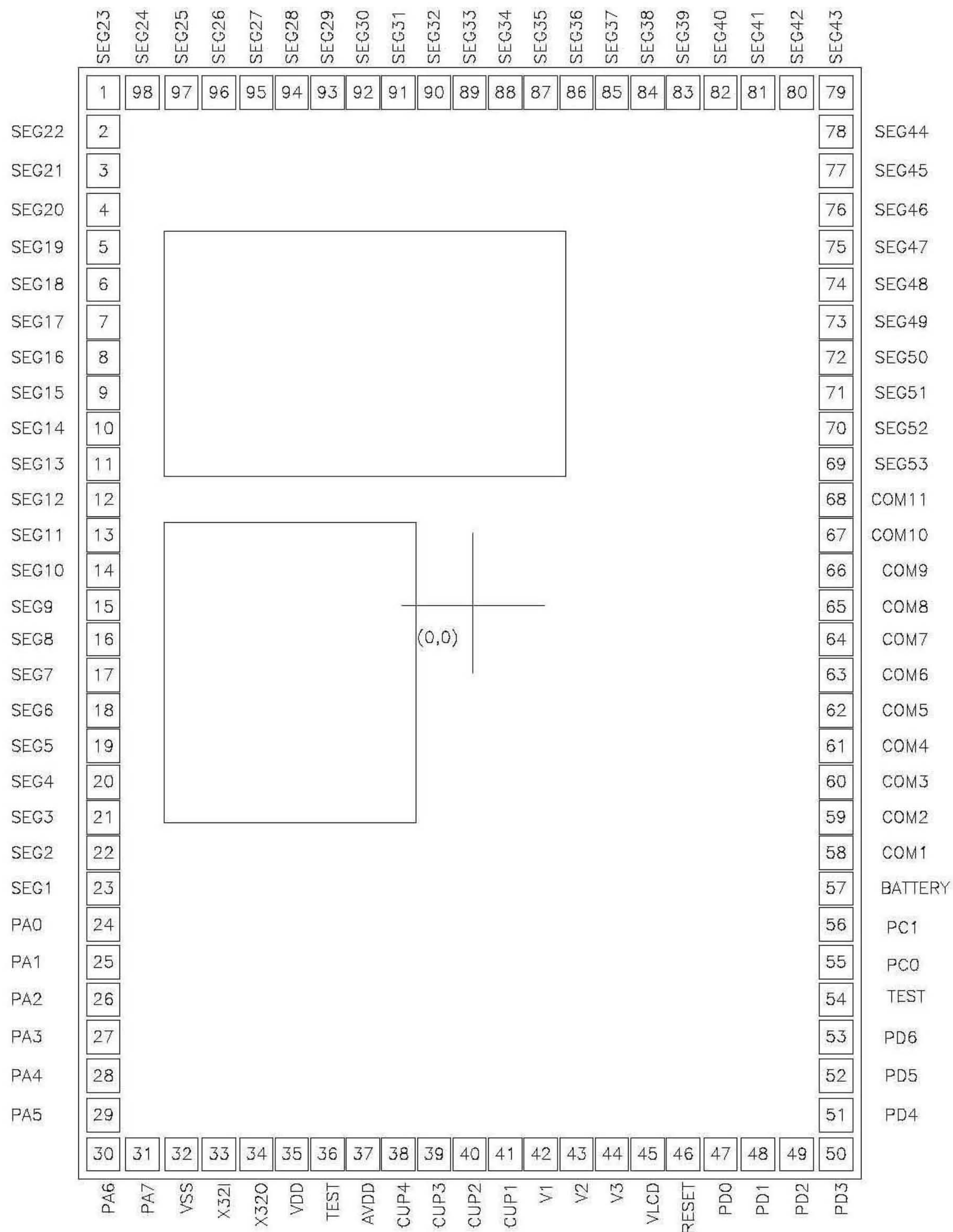
DL5546





PACKAGE/PAD LOCATIONS

PAD Assignment



Chip Size: 2300 μ m $\times$ 3230 μ m

This IC substrate should be connected to VSS

Note1: Chip size included scribe line.

Note2: To ensure that the IC functions properly, bond all VDD and VSS pins.

Note3: The 0.1 μ F capacitor between VDD and VSS should be placed to IC as close as possible.



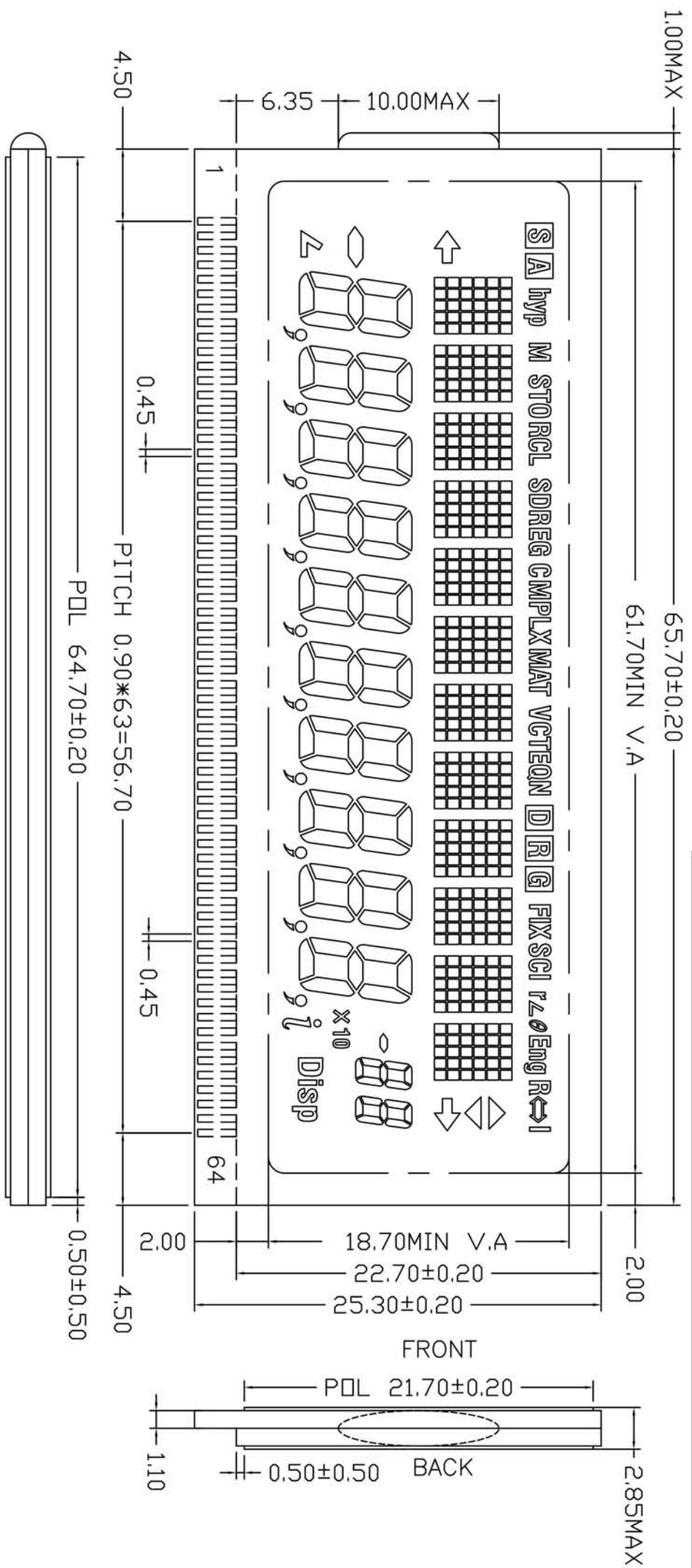
PAD Locations

PAD No.	PAD Name	X	Y	PAD No.	PAD Name	X	Y
1	SEG23	-1130.000	1445.000	38	CUP4	-300.000	-1545.000
2	SEG22	-1130.000	1335.000	39	CUP3	-200.000	-1545.000
3	SEG21	-1130.000	1225.000	40	CUP2	-100.000	-1545.000
4	SEG20	-1130.000	1115.000	41	CUP1	0.000	-1545.000
5	SEG19	-1130.000	1010.000	42	V1	100.000	-1545.000
6	SEG18	-1130.000	905.000	43	V2	200.000	-1545.000
7	SEG17	-1130.000	800.000	44	V3	300.000	-1545.000
8	SEG16	-1130.000	700.000	45	VLCD	400.000	-1545.000
9	SEG15	-1130.000	600.000	46	RESET	500.000	-1545.000
10	SEG14	-1130.000	500.000	47	PD0	605.000	-1545.000
11	SEG13	-1130.000	400.000	48	PD1	710.000	-1545.000
12	SEG12	-1130.000	300.000	49	PD2	820.000	-1545.000
13	SEG11	-1130.000	200.000	50	PD3	930.000	-1545.000
14	SEG10	-1130.000	100.000	51	PD4	930.000	-1435.000
15	SEG9	-1130.000	0.000	52	PD5	930.000	-1325.000
16	SEG8	-1130.000	-100.000	53	PD6	930.000	-1215.000
17	SEG7	-1130.000	-200.000	54	TEST	930.000	-1110.000
18	SEG6	-1130.000	-300.000	55	PC0	930.000	-1005.000
19	SEG5	-1130.000	-400.000	56	PC1	930.000	-900.000
20	SEG4	-1130.000	-500.000	57	BATTERY	930.000	-800.000
21	SEG3	-1130.000	-600.000	58	COM1	930.000	-700.000
22	SEG2	-1130.000	-700.000	59	COM2	930.000	-600.000
23	SEG1	-1130.000	-800.000	60	COM3	930.000	-500.000
24	PA0	-1130.000	-900.000	61	COM4	930.000	-400.000
25	PA1	-1130.000	-1005.000	62	COM5	930.000	-300.000
26	PA2	-1130.000	-1110.000	63	COM6	930.000	-200.000
27	PA3	-1130.000	-1215.000	64	COM7	930.000	-100.000
28	PA4	-1130.000	-1325.000	65	COM8	930.000	0.000
29	PA5	-1130.000	-1435.000	66	COM9	930.000	100.000
30	PA6	-1130.000	-1545.000	67	COM10	930.000	200.000
31	PA7	-1020.000	-1545.000	68	COM11	930.000	300.000
32	VSS	-910.000	-1545.000	69	SEG53	930.000	400.000
33	X32I	-805.000	-1545.000	70	SEG52	930.000	500.000
34	X32O	-700.000	-1545.000	71	SEG51	930.000	600.000
35	VDD	-600.000	-1545.000	72	SEG50	930.000	700.000
36	TEST	-500.000	-1545.000	73	SEG49	930.000	800.000
37	AVDD	-400.000	-1545.000	74	SEG48	930.000	905.000



PAD No.	PAD Name	X	Y	PAD No.	PAD Name	X	Y
75	SEG47	930.000	1010.000	87	SEG35	100.000	1445.000
76	SEG46	930.000	1115.000	88	SEG34	0.000	1445.000
77	SEG45	930.000	1225.000	89	SEG33	-100.000	1445.000
78	SEG44	930.000	1335.000	90	SEG32	-200.000	1445.000
79	SEG43	930.000	1445.000	91	SEG31	-300.000	1445.000
80	SEG42	820.000	1445.000	92	SEG30	-400.000	1445.000
81	SEG41	710.000	1445.000	93	SEG29	-500.000	1445.000
82	SEG40	605.000	1445.000	94	SEG28	-600.000	1445.000
83	SEG39	500.000	1445.000	95	SEG27	-700.000	1445.000
84	SEG38	400.000	1445.000	96	SEG26	-805.000	1445.000
85	SEG37	300.000	1445.000	97	SEG25	-910.000	1445.000
86	SEG36	200.000	1445.000	98	SEG24	-1020.000	1445.000

出货标准按
客户签名确认所有图纸



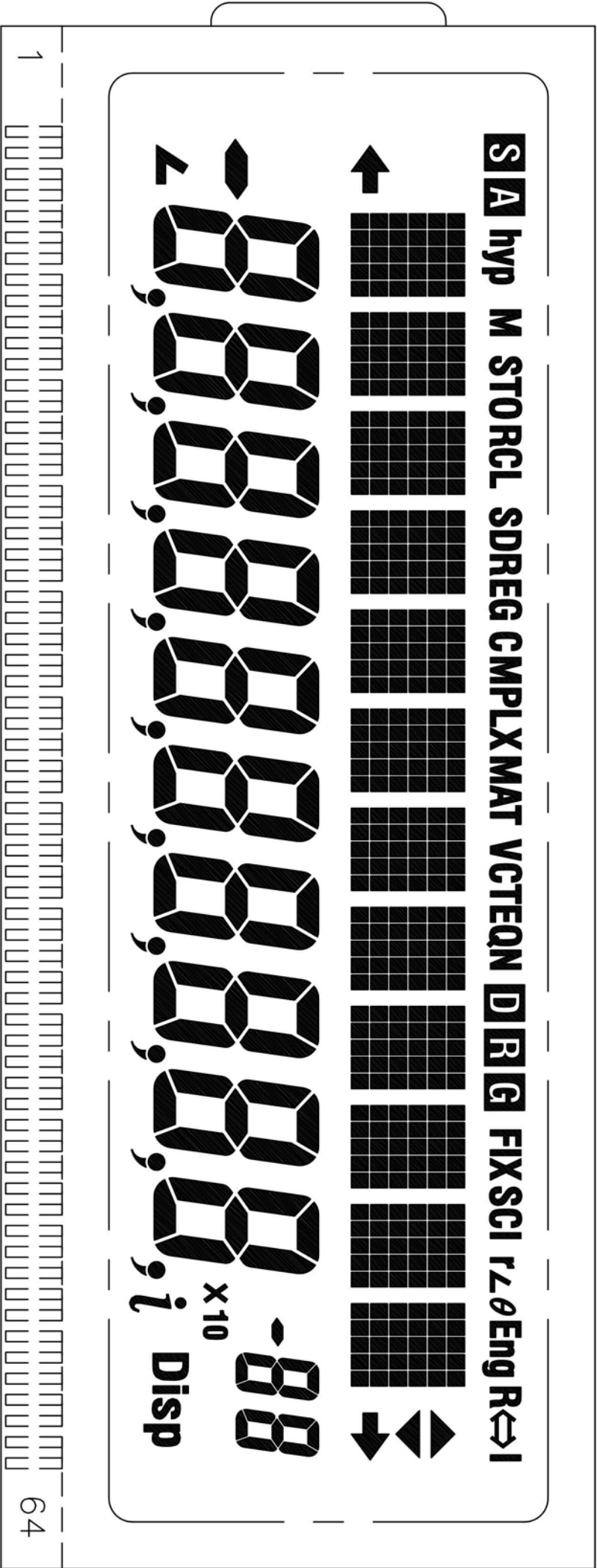
NOTES:

符合ROHS要求

1. DISPLAY TYPE: TN
2. POLARIZER MODE: REFLECTIVE/POSITIVE.
3. DRIVE METHOD: 1/10DUTY, 1/4BIAS.
4. OPERATING VOLTAGE: 3.4V.
5. VIEWING DIRECTION: 6 0'CLOCK.
6. OPERATING TEMP: 0°C~+50°C.
7. STORAGE TEMP: -10°C~+60°C.
8. CONNECTOR:

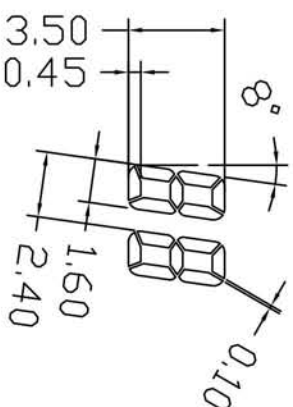
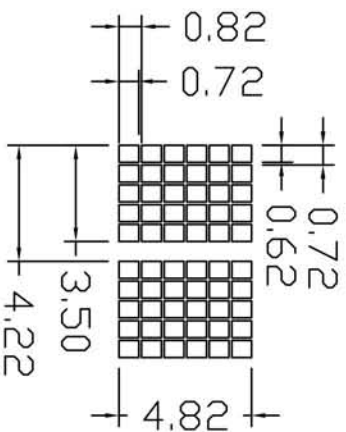
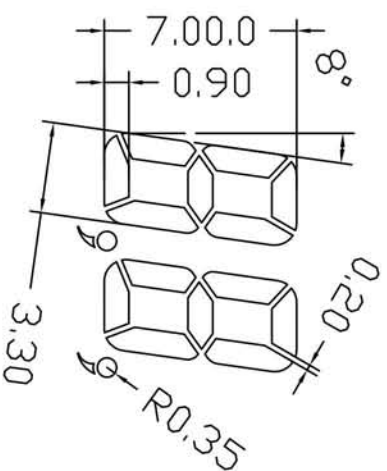
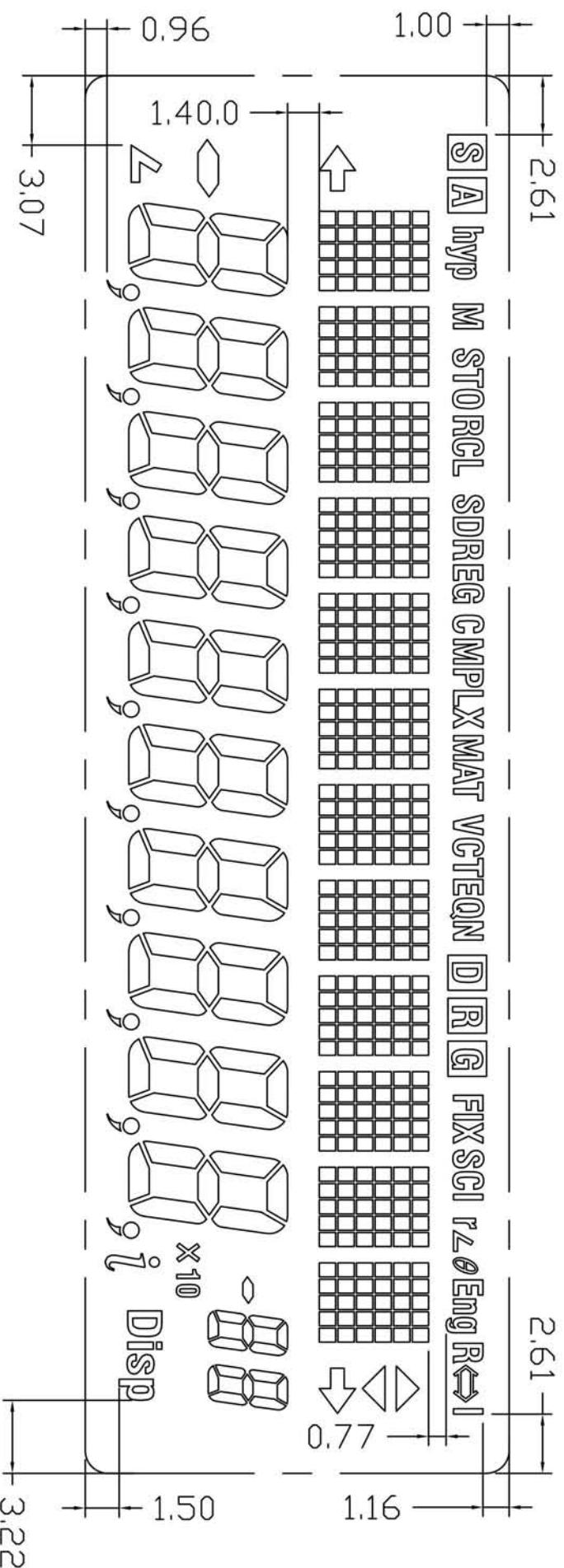
CUSTOMER NO:		TOLERANCES:±0.2 ANGLES:±0.1°		UNIT:MM		MODEL NO:		CHECKED DESIGENED		REV:00	
DATE		APPROVED		CHECKED		DESIGENED		REV:00		PAGE:4-1	

全显时, 可视区内有3个0. 07MM*0. 07MM的交叉亮点!

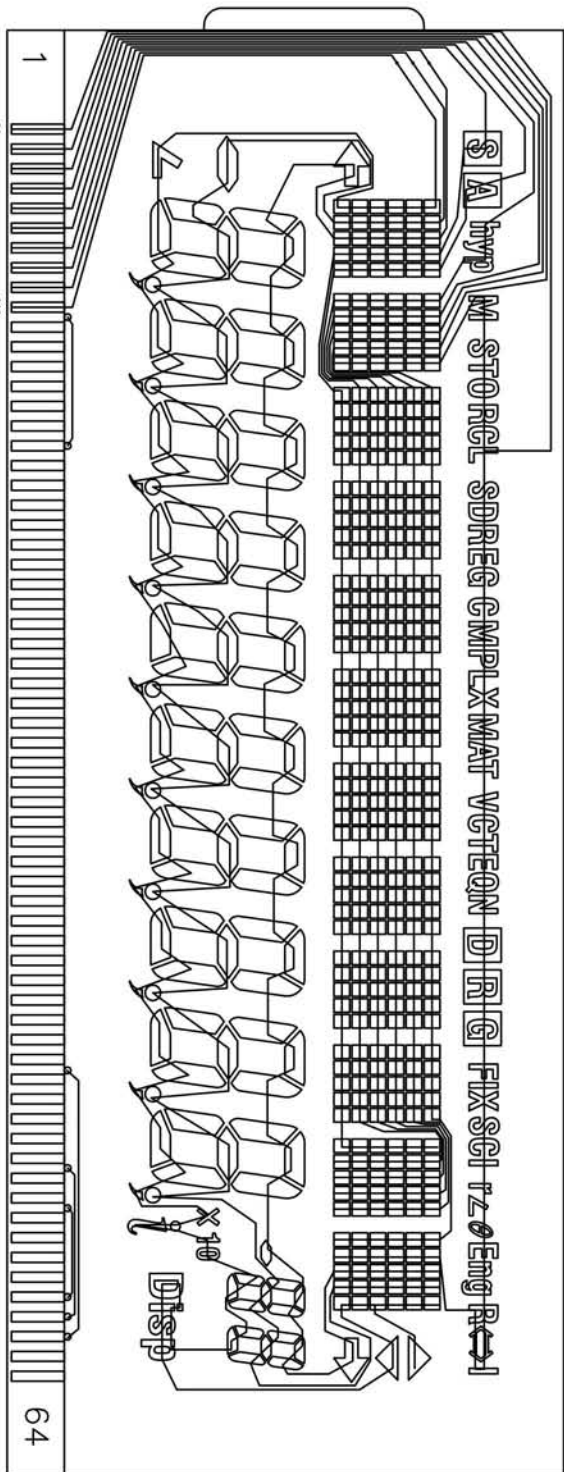
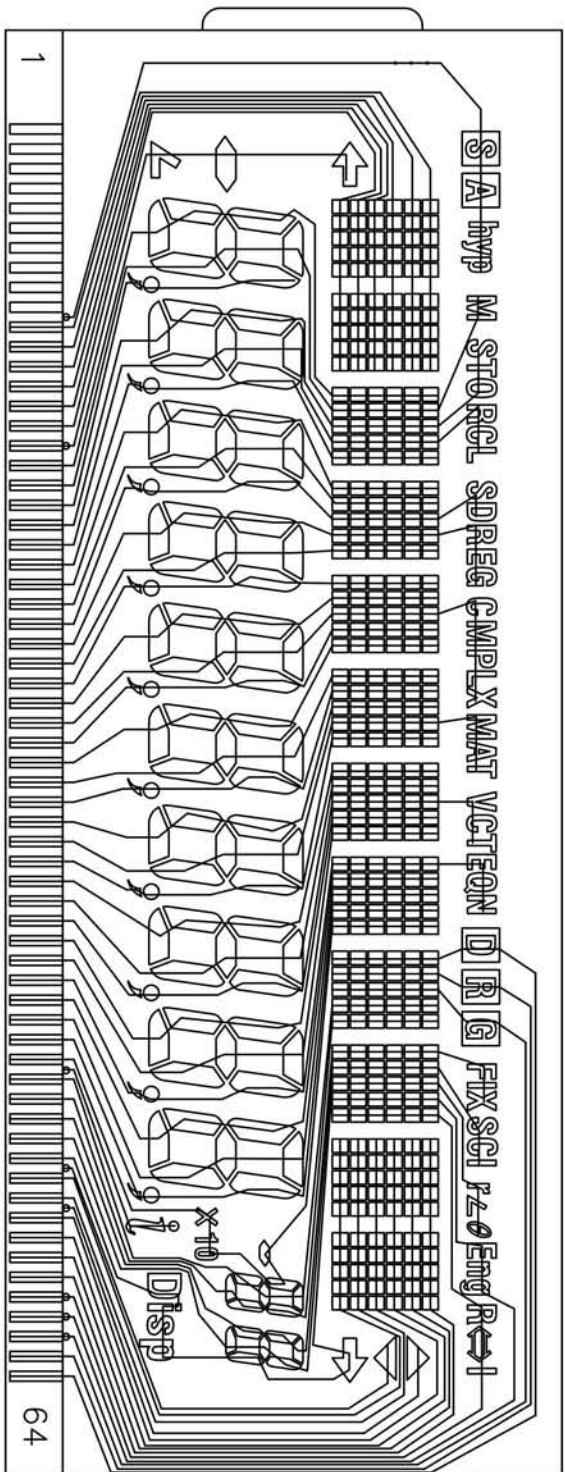


显示效果图

CUSTOMER NO:			TOLERANCES:±0.2 ANGLES:±0.1°	UNIT:MM	MODEL NO:	
DATE	APPROVED	CHECKED	DESIGENED	REV:00	PAGE:4-4	



CUSTOMER NO:			TOLERANCES:±0.2		UNIT:MM		MODEL		
			ANGLES:±0.1°				NO:		
DATE			APPROVED		CHECKED		DESIGENED		
									PAGE:4-2



SEG & COM

此图仅供逻辑走线参考

CUSTOMER NO:				TOLERANCES:±0.2		UNIT:MM		MODEL NO:	
DATE		APPROVED		LPP		CHECKED		DESIGNED	
						CXY		D M	
								PAGE:4-3	