



MC AC	PA0	PA1	PA2	PA3	PA4	PA5	PA6	PA7	
	ALPHA	Shift	Left	Right	Mode	Off			PB2
	sqrtX G Xy	sqrt3 H sqrt	NORM g FIX	ENG Na SCI	DRG-K DRG	BLOCK Vm DHBO			PB3
	hpy	d/c h ab/c	G FMLA	IN e CALC	RANDOM s (-)	INSERT u BS			PB4
	Sin-1 A sin	Cos-1 B Cos	Tan-1 C Tan	DEG D D'M'S	10x E log	Ex F ln	r8 , 0		PB1
	X-1 X2	NEG O (NOP P)	RCL J STO	CD K DATA	M- MR M+	xy : ·		PB0
	n! L 7	n M 8	x N 9	C			Pai C EXP		PC1
	nPr Q 4	Σx R 5	Σx2 S 6	AND T *	XOR U /		PDEL me DEL		PC2
	nCr V 1	n W 2	n-1 X 3	OR Y +	XNOR Z -	% =			PC3

Title			
DL5524 application circuit			
Size	Number	Revision	
B	DL5524	V1.2	
Date:	20-Jan-2011	Sheet of	1/1
File:	X:\EL0280A\SCH\EL0280A.SCH	Drawn By:	Dick

