The DL1135 series are analog clock ICs that derive their timing form a 32 KHz oscillator element．They feature alarm output snooze function and alarm auto－stop function．They can be configured to match a wide variety of clock specifications，alarm functions outputs．

## Features

Single 1.5 V battery operation
$32,768 \mathrm{~Hz}$ crystal frequency
Low power dissipation
Built－in trim capacitor
Output for 1 Hz or 16 Hz stepper motor with selectable pulse width
256 second snooze interval
128 second alarm output auto－stop function（Mask Option）
Alarm outputs compatible with both electronic sound alarms and motor bells
4－step increasing volume alarm output
ALIB and SNZB use different pins
Built－in debounce circuit（ALIB／SNZB pin）
Fast test functions
Power－on－clear function

## PAD LAYOUT

| $\square$ SNZA $\square$ TEST | OSCO $\square$ |
| :--- | ---: |
| $\square$ ALIB | OSCI $\quad \square$ |
| $\square$ ALO | VDD $\square$ |
| $\square$ MOT1$1.32 \mathrm{~mm} \times 1.22 \mathrm{~mm}$ <br> Substrate is $\mathrm{V}_{\mathrm{DD}}$ | VSS $\square$ |
| $\square$ MOT2 |  |


| PAD <br> No． | PAD <br> Name． | X | Y |
| :--- | :--- | :--- | :--- |
| 1 | SNZB | 75 | 1075 |
| 2 | ALIB | 75 | 911 |
| 3 | ALO | 75 | 760 |
| 4 | MOT1 | 105 | 553 |
| 5 | MOT2 | 93 | 75 |
| 6 | T1 | 1175 | 186 |
| 7 | VSS | 1175 | 628 |
| 8 | VDD | 1175 | 778 |
| 9 | OSCI | 1175 | 923 |
| 10 | OSCO | 1175 | 1075 |
| 11 | TEST | 249 | 1065 |

MOT2

## TYPE LIST

| TYPE | ALO | MOT frequency | MOT pu1se width |
| :--- | :--- | :--- | :--- |
| DL1135 | 4 step | 16 Hz | 31.25 ms |

## 4－step Alarm Waveform

| Time（sec）After Turning Alarm on | Alarm Waveform | Duty（\％）of Fundamental Wave |
| :---: | :---: | :---: |
| 1～8 |  | $\underset{12.5 \%}{ـ ـ ـ}$ |
| 9～16 |  | $\underset{25 \%}{ـ \prec}$ |
| 17～24 |  | $\underset{50 \%}{ـ}$ |
| 25～ |  | $\underset{50 \%}{\boxed{L}}$ |

## Snooze Waveform

ALIB

SNZB
ALO $\qquad$ ๑ルケூ $\qquad$ Һூூூ $\qquad$ $K-256 \mathrm{Sec} \longrightarrow$


Motor Output Driving


## Application Diagram

Speaker application


## DC Characteristics

（ $\mathrm{V}_{\mathrm{DD}}=1.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{SS}}=0 \mathrm{~V}$, Fosc $=32768 \mathrm{~Hz} \mathrm{Ta}=25^{\circ} \mathrm{C}$ unless specified otherwise）

| Item | Symb． | Condition | Min． | Typ． | Max． | Unit |
| :--- | :---: | :--- | :---: | :---: | :---: | :---: |
| Supply Voltage | $\mathrm{V}_{\mathrm{DD}}$ |  | 1.1 |  | 1.8 | V |
| Operating Current | IDD | No Load |  | 1.2 | 2.0 | $\mu \mathrm{~A}$ |
| Output Current |  | $\mathrm{V}_{\mathrm{DD}}=1.2 \mathrm{~V}$ |  |  |  |  |
| Motor | $\mathrm{I}_{\mathrm{M}}$ | $\mathrm{RL}=200 \Omega$ | 4.5 |  |  | mA |
| Alarm high | IoHA | $\mathrm{VoHA}=0.7 \mathrm{~V}$ | 0.1 | 0.25 | 0.35 | mA |
| Alarm low | IoLA | $\mathrm{VoLA}=0.5 \mathrm{~V}$ | 0.1 | 0.25 | 0.35 | mA |
| OSC．Start time |  | $\mathrm{V}_{\mathrm{DD}}=1.2 \mathrm{~V}$ |  |  | 2 | sec |
| OSC．Stability | $\triangle \mathrm{f} / \mathrm{f}$ | $\triangle \mathrm{VDD}_{\mathrm{DD}}=0.1 \mathrm{~V}$ |  | 0.5 | 1 | ppm |
| Internal Cap． | Cd |  |  | 25 |  | pF |
| Internal Cap． | Cg | Mask Option | 5 |  | 25 | pF |

