DL1135

The DL1135 series are analog clock ICs that derive their timing form a 32KHz oscillator element. They feature alarm output snooze function and alarm auto-stop function. They can be configured to match a wide variety of clock specifications, alarm functions outputs.

Features

Single 1.5V battery operation 32,768 Hz crystal frequency Low power dissipation Built-in trim capacitor Output for 1Hz or 16Hz stepper motor with selectable pulse width 256 second snooze interval 128 second alarm output auto-stop function (Mask Option) Alarm outputs compatible with both electronic sound alarms and motor bells 4-step increasing volume alarm output ALIB and SNZB use different pins Built-in debounce circuit (ALIB/SNZB pin) Fast test functions Power-on-clear function

PAD LAYOUT

SNZE	EST	OSCO	PAD No.	PAD Name.	Х	Y
			1	SNZB	75	1075
ALIB	\$	OSCI	2	ALIB	75	911
			3	ALO	75	760
ALO	0	VDD	4	MOT1	105	553
MOT1 1.32mm×1.22mm Substrate is V _{DD}		5	MOT2	93	75	
	NOTI	VSS	6	T1	1175	186
			7	VSS	1175	628
			8	VDD	1175	778
			9	OSCI	1175	923
			10	OSCO	1175	1075
		T1	11	TEST	249	1065
MOT2						

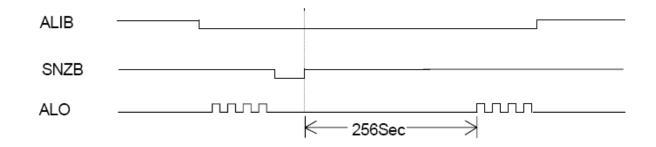
TYPE LIST

ТҮРЕ	ALO	MOT frequency	MOT pulse width
DL1135	4 step	16Hz	31.25ms

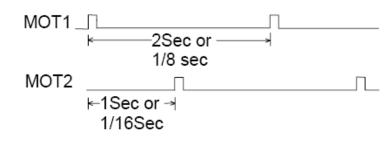
4-step Alarm Waveform

Time (sec) After Turning Alarm on	Alarm Waveform	Duty (%) of Fundamental Wave
1~8		ſſ 12.5%
9~16		 25%
17~24	 ⊱ 500mS → 500mS → 2048×8×1Hz	 50%
25~	500mS → ← 500mS → 2048×8Hz	 50%

<u>Snooze Waveform</u>

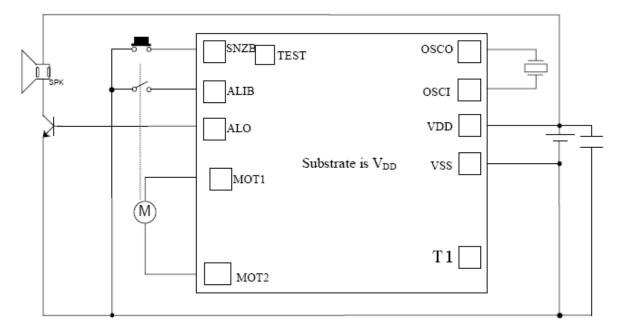


Motor Output Driving



Application Diagram

Speaker application



DC Characteristics

Item	Symb.	Condition	Min.	Тур.	Max.	Unit
Supply Voltage	V _{DD}		1.1		1.8	V
Operating Current	Idd	No Load		1.2	2.0	μΑ
Output Current		$V_{DD}=1.2V$				
Motor	I_{M}	RL=200Ω	4.5			mA
Alarm high	Іона	Voha=0.7V	0.1	0.25	0.35	mA
Alarm low	Iola	Vola=0.5V	0.1	0.25	0.35	mA
OSC. Start time		$V_{DD}=1.2V$			2	sec
OSC. Stability	$\triangle f/f$	$\triangle V_{DD}=0.1V$		0.5	1	ppm
Internal Cap.	Cd			25		pF
Internal Cap.	Cg	Mask Option	5		25	pF