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DL1102 五位半电子表 IC

产 品 说 明 书

6 FUNCTIONS 6 DIGITS ALARM WATCH

The DL1102 is a CMOS digital 6-function watch integrated circuit with alarm and autoranging chronograph function; designed to for a 6-digit duplexed liquid crystal display, 7-day mark, date mark, AM/PM mark and colon.

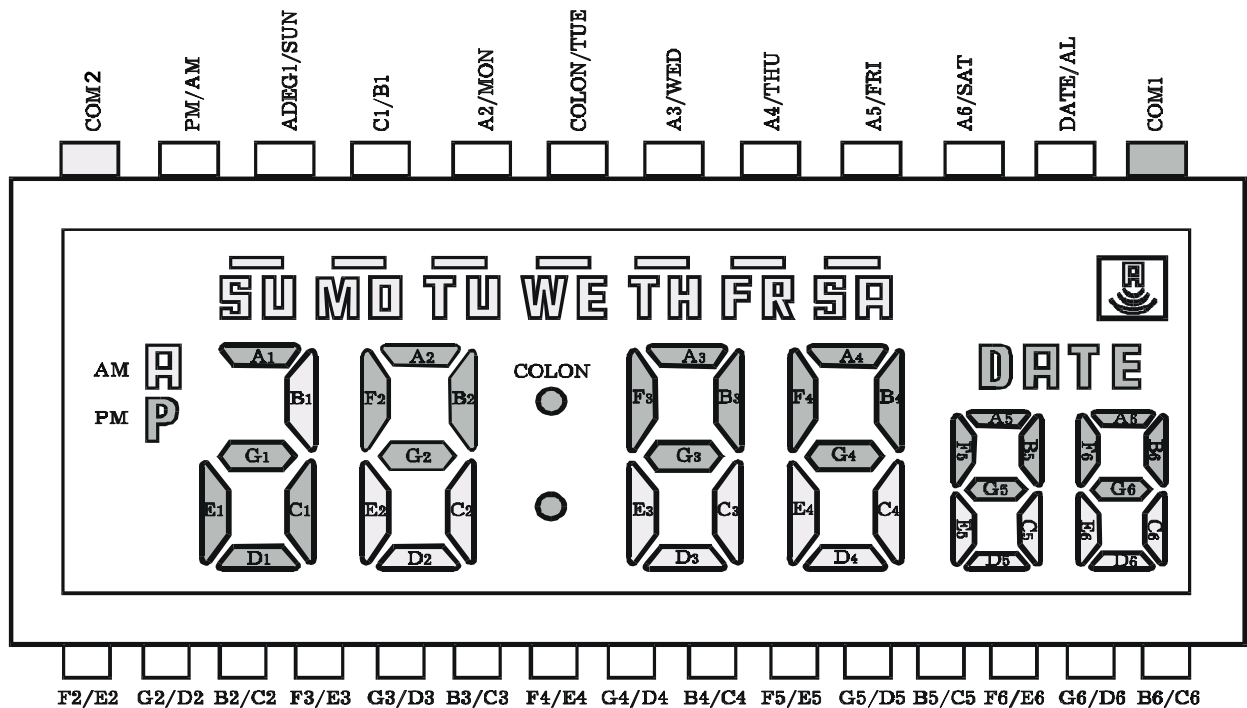
FEATURES

- Single-chip CMOS construction
- Drives 6-digit duplexed LCD with 7-day mark, AM/PM mark, date mark and alarm mark
- Colon display
- Two outputs for drive of piezoelectric transducer (built-in transistor for direct drive or drive by external transistor)
- 32,768Hz crystal frequency
- On-chip oscillator, capacitor, resistor and voltage doubler
- Single 1.5V battery operation
- Low power dissipation
- Debounce circuitry on switch inputs
- Protection against static discharge

FUNCTIONS

- 6 Functions: Month, Date, Day-of-Week, Hour, Minute, Second
- 6-digit Chronograph: Auto-ranging after 30 minutes to hour, minute; second
- User selectable 12-hour/24-hour format
- 4-year calendar
- One-touch correction of time error within ± 30 seconds
- Fast advance for time and alarm time set
- Chime on every hour
- 3-switch sequential operation

LCD FORMAT



ABSOLUTE MAXIMUM RATINGS

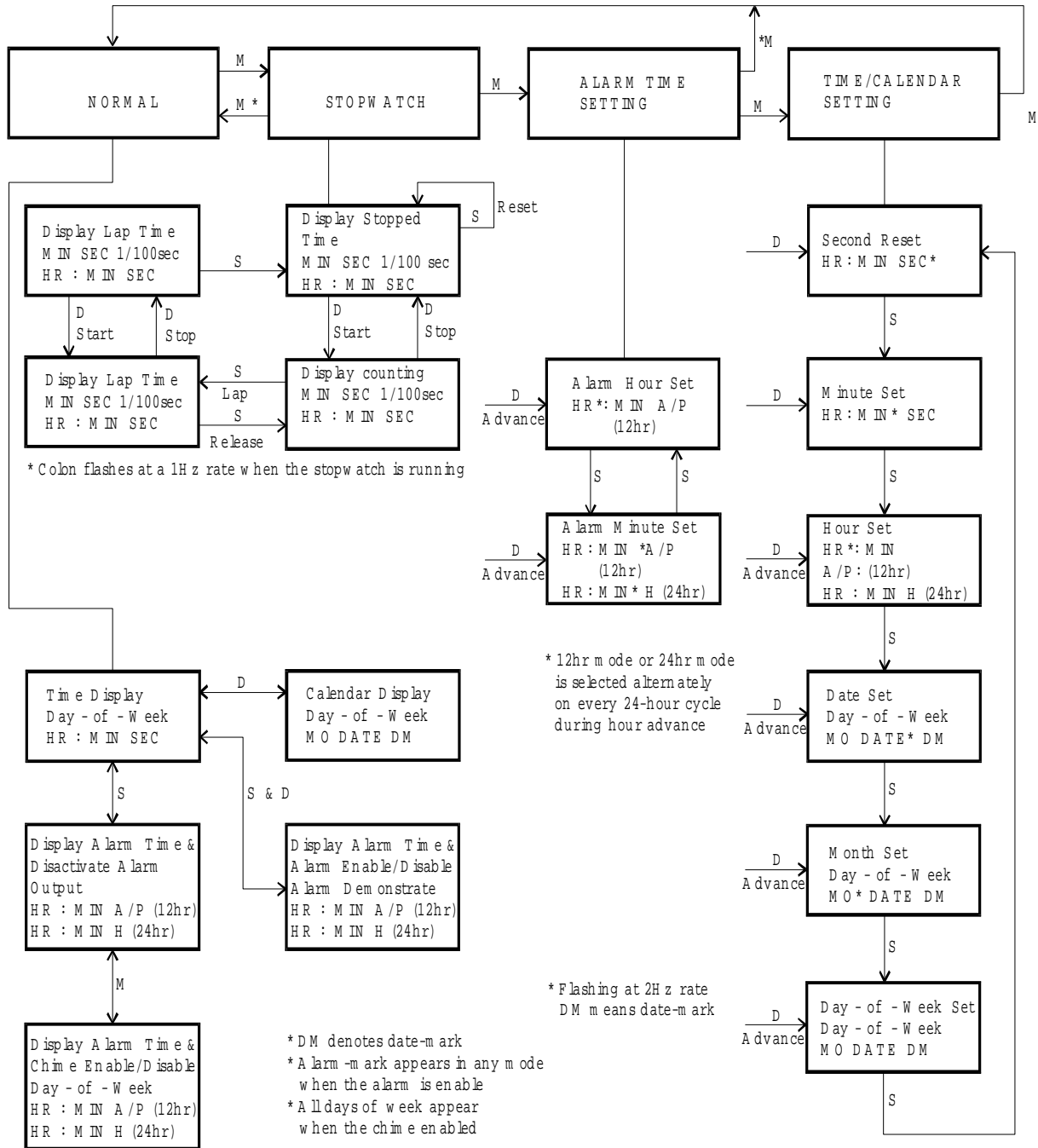
Characteristic	Symbol	Value	Unit
Supply Voltage, V_{DD}	V_{DD}	- 0.3 ~ + 4.0	V
Operating Temperature	T_{opr}	- 20 ~ + 70	°C
Storage Temperature	T_{stg}	- 55 ~ + 125	°C

ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$, $V_{SS} = 0\text{V}$, $V_{DD} = 3\text{V}$ unless otherwise specified)

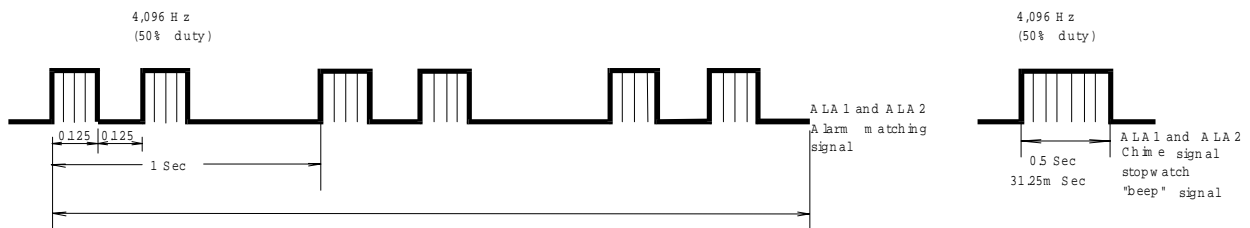
Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Operating Voltage	V_{CC}		1.2	1.5	1.8	V
Display Voltage	V_{DD}		2.4	3.0	3.6	V
Supply Current	I_{CC}	Without load		1.0	2.0	μA
Input High Voltage	V_{IH}		$V_{CC}-0.3\text{V}$		V_{CC}	V
Input Low Voltage	V_{IL}		V_{SS}		$V_{CC}+0.3\text{V}$	V
Oscillator Start Voltage	V_{OSC}				1.45	V
Oscillator Stop Voltage	V_{OSP}				1.15	V
Alarm Drive Current (ALA1)	I_O	$V_{sat} = 0.5\text{V}$	10	20		mA
Oscillator Frequency	F_{OSC}			32,768		Hz
DC-DC Conversion Frequency	F_{CON}	$C1 = C2 = 0.1\mu\text{F}$		1,024		Hz
Oscillator Input and Output Capacitors	C_{IN}			25		pF
Time Stability	T_{stb}	$V_{CC} = 1.3 \div 1.8\text{V}$		1	3	ppm
Switch Debouncing Time	T_{deb}				31.25	mSec

SETTING SEQUENCE AND SWITCH OPERATION

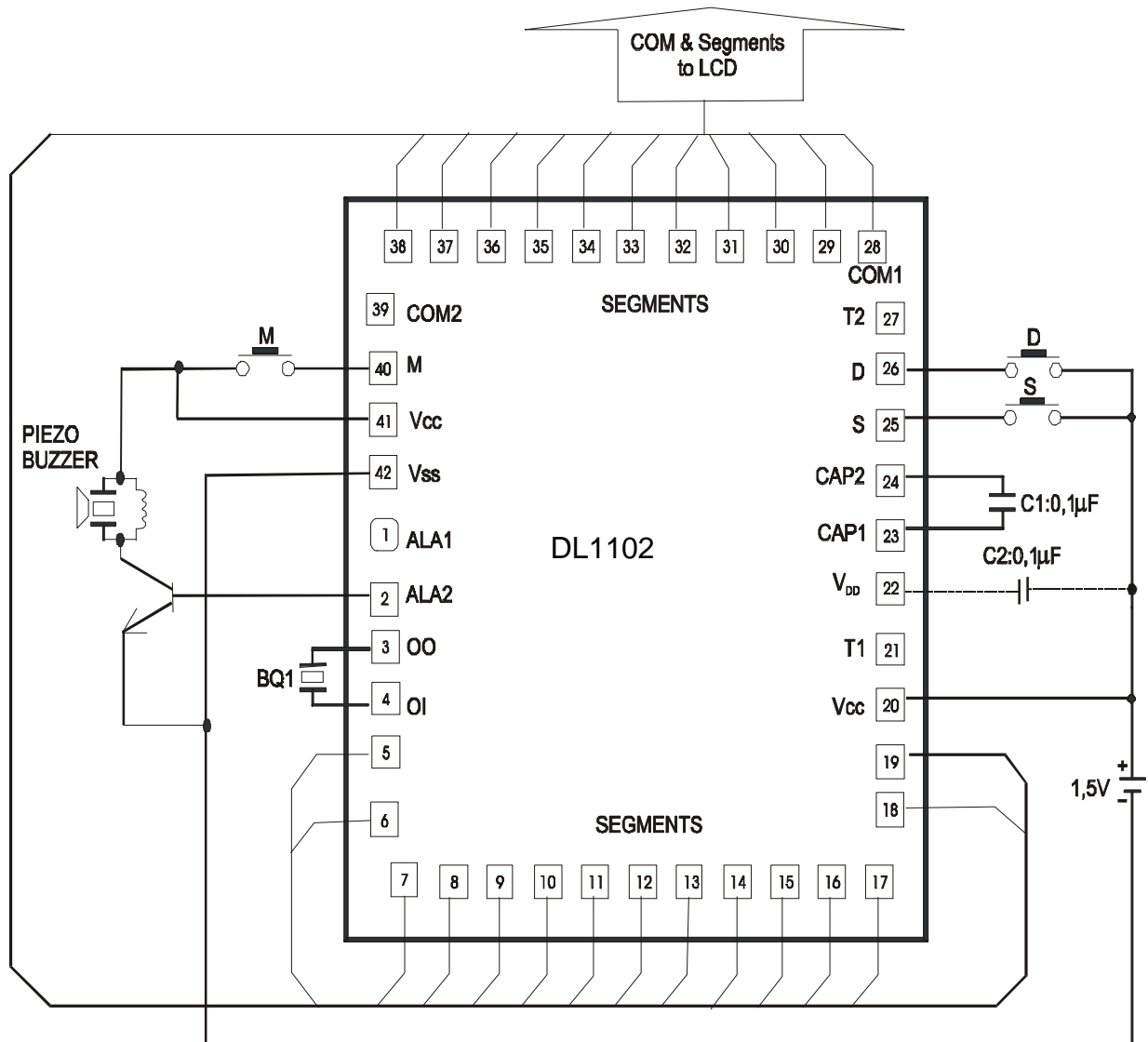
* When S or D is used in the stopwatch mode or the alarm time setting mode, the normal mode is obtained by depressing M



ALARM OUTPUT WAVEFORMS



APPLICATION CIRCUIT (drive of piezobuzzer by external transistor)



NOTE: The die area should be isolated or connected to V_{SS}.

* Quartz Crystal Parameter

F_p = 32,768 Hz

CL = 12.5 pF

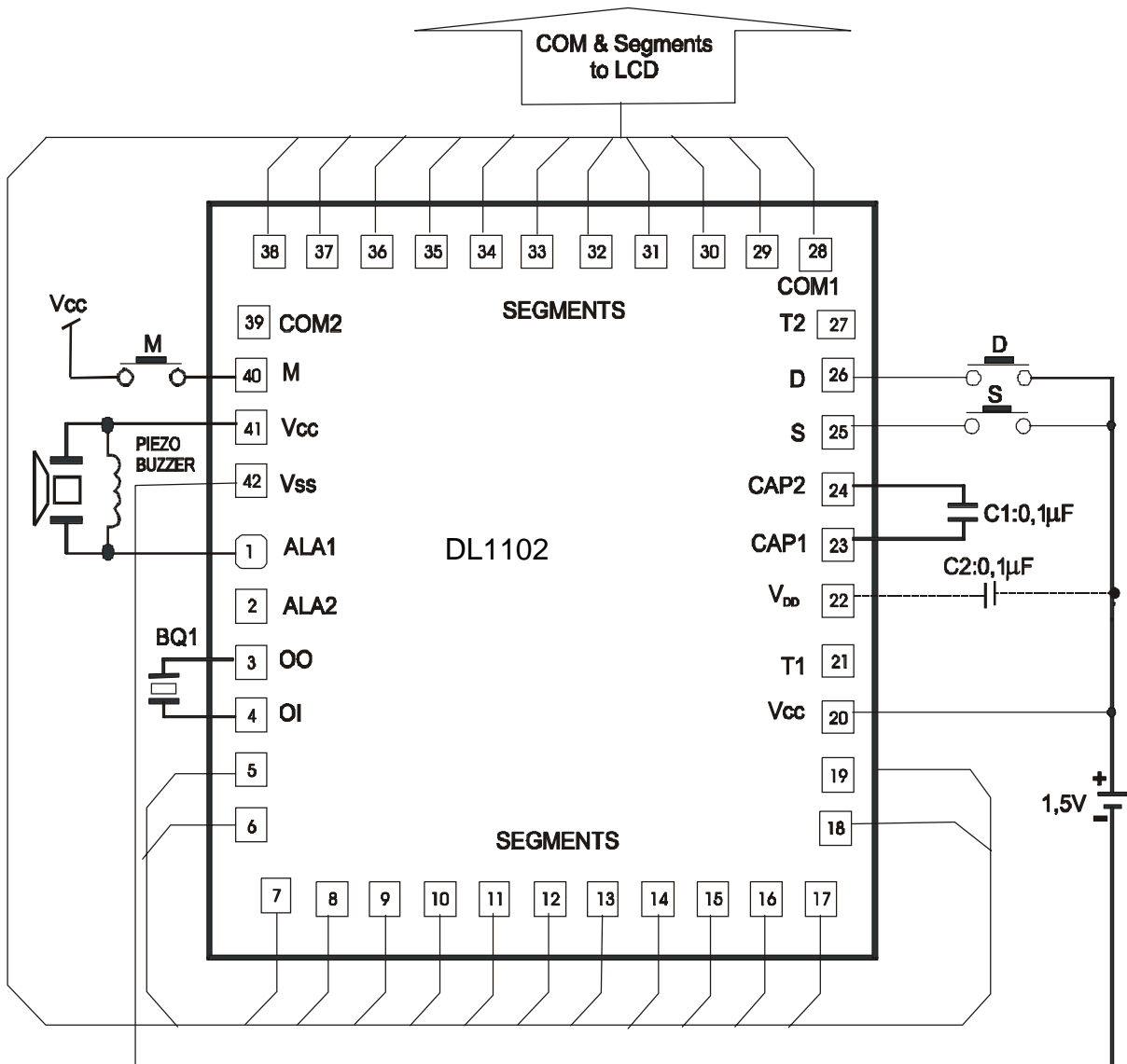
C1 = 4 pF

C0 = 2.5 pF

R_c = 35 KΩ

Q = 35,000

APPLICATION CIRCUIT (drive of piezobuzzer by built-in transistor)



NOTE: The die area should be isolated or connected to V_{SS} .

* Quartz Crystal Parameter

$F_p = 32,768 \text{ Hz}$

$CL = 12.5 \text{ pF}$

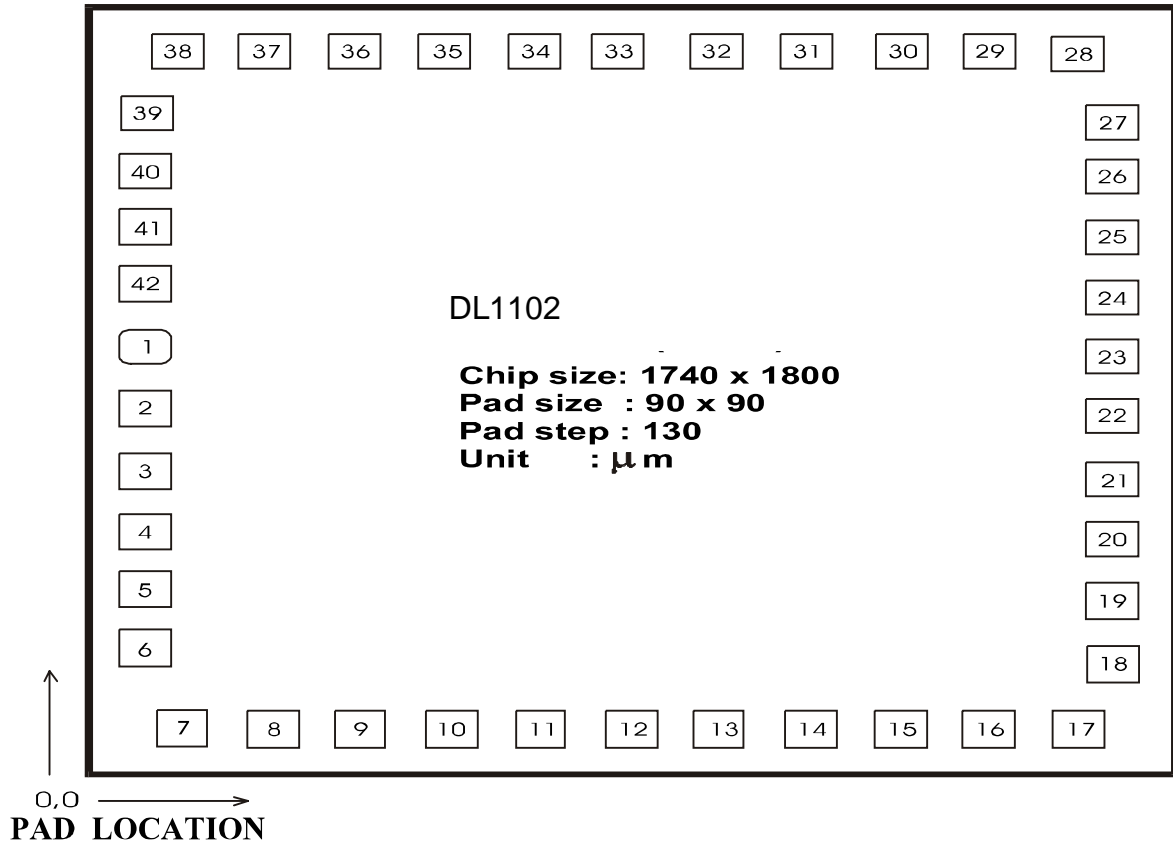
$C1 = 4 \text{ pF}$

$C0 = 2.5 \text{ pF}$

$R_c = 35 \text{ K}\Omega$

$Q = 35,000$

PAD DIAGRAM



Pad No.	Pad Name	X	Y	Pad No.	Pad Name	X	Y	Pad No.	Pad Name	X	Y
01	ALA1	105	927	15	G5/D5	1216	105	29	DATE/AL	1345	1605
02	ALA2	105	789	16	B5/C5	1346	105	30	A6/SAT	1215	1605
03	OO	105	659	17	F6/E6	1476	105	31	A5/FRI	1085	1605
04	OI	105	529	18	G6/D6	1545	269	32	A4/THU	955	1605
05	F2/E2	105	399	19	B6/C6	1545	399	33	A3/WED	825	1605
06	G2/D2	105	269	20	Vcc	1545	529	34	COLON/TUE	695	1605
07	B2/C2	176	105	21	T1	1545	659	35	A2/MON	565	1605
08	F3/E3	306	105	22	V _{DD}	1545	789	36	C1/B1	435	1605
09	G3/D3	436	105	23	CAP1	1545	919	37	ADEG1/SUN	305	1605
10	B3/C3	565	105	24	CAP2	1545	1049	38	PM/AM	175	1605
11	F4/E4	696	105	25	S	1545	1179	39	COM2	105	1439
12	G4/D4	826	105	26	D	1545	1309	40	M	105	1309
13	B4/C4	956	105	27	T2	1545	1439	41	Vcc	105	1179
14	F5/E5	1086	105	28	COM1	1475	1605	42	Vss	105	1049

NOTE: The chip substrate is electrically connected to V_{SS}.

Pad size is given as per passivation layer

Thickness of chip is 0,460 ± 0,02 mm